

J-STD-002D

Solderability Tests for Component Leads, Terminations, Lugs, Terminals and Wires

Draft 1

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Track Changes Visible

1 SCOPE

1.1 Scope This standard prescribes test methods, defect definitions, acceptance criteria, and illustrations for assessing the solderability of electronic component leads, terminations, solid wires, stranded wires, lugs, and tabs. This standard also includes a test method for the Resistance to Dissolution/Dewetting of Metallization. This standard is intended for use by both vendor and user.

1.2 Purpose Solderability evaluations are made to verify that the solderability of component leads and terminations meets the requirements established in this standard and to determine that storage has had no adverse effect on the ability to solder components to an interconnecting substrate. Determination of solderability can be made at the time of manufacture, at receipt of the components by the user, or just before assembly and soldering.

The resistance to dissolution of metallization determination is made to verify that metallized terminations will remain intact throughout the assembly soldering processes.

1.2.1 Shall and Should The word “**shall**” is used in the text of this document wherever there is a requirement for materials, preparation, process control or acceptance of a soldered connection or a test method. The word “**should**” reflects recommendations and is used to reflect general industry practices and procedures for guidance only.

1.2.2 Document Hierarchy In the event of conflict, the following decreasing order of precedence applies:

1. Procurement as agreed between user and supplier.
2. Master drawing or master assembly drawing reflecting the user’s detailed requirements.
3. When invoked by the customer or per contractual agreement, this document, J-STD-002.
4. Other documents to extent specified by the customer.

1.3 Method Classification This standard describes methods by which component leads or terminations may be evaluated for solderability. Test A, Test B, Test C, Test D and Test S for tin/lead solder processes and Test A1, Test B1, Test C1, Test D and Test S1 for lead-free solder processes, unless otherwise agreed upon between vendor and user, are to be used for each application as a default.

1.3.1 Visual Acceptance Criteria Tests

Test A – Solder Bath/Dip and Look Test (Leaded Components and Stranded Wires) Tin/Lead Solder (paragraph 4.2.1)

Test B – Solder Bath/Dip and Look Test (Leadless Components) Tin/Lead Solder (paragraph 4.2.2)

Test C – Wrapped Wires Test (Lugs, Tabs, Hooked Leads, and Turrets) Tin/Lead Solder (paragraph 4.2.3)

Test D – Resistance to Dissolution/Dewetting of Metallization Test Tin/Lead Solder and Lead-free Solder (paragraph 4.2.4)

Test S – Surface Mount Process Simulation Test Tin/Lead Solder (paragraph 4.2.5)

Test A1 – Solder Bath/Dip and Look Test (Leaded Components and Stranded Wires) Lead-free Solder (paragraph 4.2.6)

Test B1 – Solder Bath/Dip and Look Test (Leadless Components) Lead-free Solder (paragraph 4.2.7)

Test C1 – Wrapped Wires Test (Lugs, Tabs, Hooked Leads, and Turrets) Lead-free Solder (paragraph 4.2.8)

Test S1 – Surface Mount Process Simulation Test Lead-free Solder (paragraph 4.2.9)

1.3.2 Force Measurement Tests

Test E – Wetting Balance Solder Pot Test (Leaded Components) Tin/Lead Solder (paragraph 4.3.1)

Test F – Wetting Balance Solder Pot Test (Leadless Components) Tin/Lead Solder (paragraph 4.3.2)

Test G – Wetting Balance Globule Test Tin/Lead Solder (paragraph 4.3.3)

Test E1 – Wetting Balance Solder Pot Test (Leaded Components) Lead-free Solder (paragraph 4.3.4)

Test F1 – Wetting Balance Solder Pot Test (Leadless Components) Lead-free Solder (paragraph 4.3.5)

Test G1 – Wetting Balance Globule Test Lead-free Solder (paragraph 4.3.6)

These methods (1.3.2) are included for evaluation purposes only. Data collected should be submitted to the IPC Wetting Balance Task Group for correlation and analysis. Tests E, F, G, E1, F1 and G1 **shall** not be used for acceptance/rejection without user and vendor agreement.

1.4 Coating Durability The following are guidelines, not specification requirements, for determining the needed level of [preconditioning](#) category assurance (see [Table 3-3](#)). The Coating Durability guidelines describe three broad usage sectors and are not intended to encompass all possible product use scenarios. The user and vendor need to agree on the coating durability requirements. Coating Durability [Category 2](#) is the default condition for tin-based finishes.

Category 1 — Minimum Coating Durability Intended for surfaces that will be soldered within a short period of time (e.g., up to six months) from the time of testing and are likely to experience a minimum of thermal exposures before soldering. **No Preconditioning category per Table X1.**

Category 2 — Typical Coating Durability ~~Intended for surfaces that will be soldered after an extended time from the time of testing and which may see limited thermal exposures before soldering (see 5.6).~~ Intended for surfaces whose solderability may become degraded from storage of longer than six months or from multiple thermal exposures. **This is preconditioning category E per Table X1.**

~~*Category 3 — Enhanced Coating Durability (default for tin-based finishes)* Intended for surfaces whose solderability may become degraded from storage of longer than six months or from multiple thermal exposures (see 5.6). Preconditioning category F or C per Table XXX.~~

Table 1-1 Steam Conditioning Categories for Component Leads and Terminations

| Category 1 | Category 2 | Category 3 |
|---|---|---|
| No Steam Conditioning Requirements | 1 Hour ± 5 min. Steam Conditioning | 8 hours ± 15 min. Steam Conditioning |

1.5 Solderability Backwards Compatibility Typically Pb containing terminations are evaluated using SnPb solderability test conditions and Pb-free terminations use Pb-free test conditions. If Pb-free terminations are to be used in a SnPb solder process (backward compatibility) then they should be evaluated using test parameters consistent with standard SnPb solder conditions. The backward compatibility test does not apply to Pb-free BGA type packages.

1.6 Referee Verification Solder Dip for Tests A, B, C, A1, B1, C1 When the dipped portion of the termination exhibits anomalies such as surface roughness, or dross, or anomalies that may have been induced by improper solder dipping, a referee verification solder dip of the suspect anomaly may be necessary. Upon reinspection, if the suspect anomaly has been removed, the anomaly will have been verified as a non-rejectable cosmetic surface defect. If the anomaly persists, regardless of area, it **shall** be classified a rejectable solderability defect. This procedure may only be used on one component per lot. Continuous need of Referee Verification Solder Dip procedure is an indication of either improper testing procedure, examination interpretation, or of poor component quality.

1.7 Limitations This standard **shall** not be construed as a production procedure for the pre-tinning of leads and terminations. Solderability testing of components is considered a destructive test and the tested component should not be used for functional electrical evaluation. Components after such solderability testing, **shall** only be used with agreement between the user and supplier (AABUS).

1.8 Contractual Agreement In cases where the stated test parameters are inappropriate or insufficient, alternative parameters may be agreed upon between vendor and user.

2 APPLICABLE DOCUMENTS

The following documents of the issue currently in effect form a part of this standard to the extent specified herein.

2.1 Industry

2.1.1 IPC¹

IPC-T-50 Terms and Definitions

IPC-TR-464 Accelerated Aging for Solderability Evaluations and Addendum

J-STD-004 Requirements For Soldering Fluxes

J-STD-005 Requirements for Soldering Pastes

J-STD-006 Requirements for Electronic Grade Solder Alloys and Fluxed and Non-Fluxed Solid Solder for Electronic Soldering Applications

IPC-TM-650 Test Methods Manual

2.1.2 International Electrotechnical Commission²

¹ www.ipc.org

² www.iec.ch

IEC 60068-2-69 Environmental testing - Part 2-69: Tests - Test Te: Solderability testing of electronic components for surface mounting devices (SMD) by the wetting balance method

2.2 Government

2.2.1 Federal

(CID) A-A-59551 Wires, Electrical, Copper (Uninsulated)

3 REQUIREMENTS

3.1 Terms and Definitions The definition of terms **shall** be in accordance with IPC-T-50. Terms repeated from IPC-T-50 are indicated by an asterisk (*).

*Dewetting** A condition that results when molten solder coats a surface and then recedes to leave irregularly-shaped mounds of solder that are separated by areas that are covered with a thin film of solder and with the basis metal not exposed.

Dissolution of Component Metallization (Leaching) The loss or removal of metallization from an area on the basis or substrate material after immersion in molten solder.

Equilibrium Wetting The degree of wetting in which the forces of wetting are in equilibrium with the forces of gravity.
Note: This wetting is visible when the wetting balance curve flattens out and approaches zero slope (see Figure 4-10).

*Nonwetting, Solder** The partial adherence of molten solder to a surface that it has contacted while leaving some basis metal exposed.

*Pinhole** An imperfection in the form of a small hole that penetrates through a layer of material.

*Solderability** The ability of a metal to be wetted by molten solder.

*Solder Connection Pinhole** A small hole that penetrates from the surface of a solder connection to a void of indeterminate size within the solder connection.

*Wetting, Solder** The formation of a relatively uniform, smooth, unbroken, and adherent film of solder to a basis metal.

3.2 Materials All chemicals **shall** be of commercial grade or better. Fresh solvents **shall** be used as often as is necessary to preclude contamination.

3.2.1 Solder For tin/lead testing, the solder composition **shall** be Sn60Pb40 or Sn63Pb37 per J-STD-006. The composition of the solder, including contamination levels, **shall** be maintained during testing per 3.5.2.

For lead-free testing, the solder composition **shall** be Sn95.5Ag3.9Cu0.6, allowing variation of the Ag content between 3.0 – 4.0 wt% and Cu content between 0.5 – 1.0 wt % with the balance being Sn per J-STD-006. If no lead-free solder alloy is specified, the default solder composition **shall** be Sn96.5Ag3.0Cu0.5 (SAC305) per J-STD-006.

The composition of the tin/lead solder paste to be used in Test S **shall** be Sn60Pb40 or Sn63Pb37 for tin/lead per J-STD-005, mesh size of -325/+500, flux type ROL1. The solder paste **shall** meet the storage and shelf life requirements of the manufacturer's specification.

The composition of the lead-free solder paste to be used in Test S1 **shall** be Sn95.5Ag3.9Cu0.6, allowing variation of the Ag content between 3.0 – 4.0 wt% and Cu content between 0.5 – 1.0 wt % with the balance being Sn per J-STD-005. If no lead-free solder paste is specified, the default composition **shall** be Sn96.5Ag3.0Cu0.5 (SAC305) per J-STD-005, mesh size of -325/+500, flux type to be agreed upon between user and vendor. The solder paste **shall** meet the storage and shelf life requirements of the manufacturer's specification.

3.2.2 Flux The flux for tin/lead solderability tests **shall** be a standard activated rosin flux #1 having a composition of 25% ± 0.5% by weight of colophony and 0.15% ± 0.01% by weight diethylammonium hydrochloride (CAS 660-68-4), in 74.85% ± 0.5% by weight of isopropyl alcohol (see Table 3-1).

The flux for lead-free solderability tests **shall** be standard activated rosin flux #2 having a composition of 25% ± 0.5% by weight of colophony and 0.39% ± 0.01% by weight diethylammonium hydrochloride (CAS 660-68-4), in 74.61% ± 0.5% by weight of isopropyl

alcohol (see Table 3-1).

Table 3-1 Flux Compositions

| Constituent | Composition by Weight Percent | |
|--|-------------------------------|-------------|
| | Flux #1 | Flux #2 |
| Colophony | 25 ± 0.5 | 25 ± 0.5 |
| Diethylammonium Hydrochloride (CAS 660-68-4) | 0.15 ± 0.01 | 0.39 ± 0.01 |
| Isopropyl Alcohol (IPA) (CAS 67-63-0) | Balance | Balance |
| Weight of Chlorine as maximum % of Solids | 0.2 | 0.5 |

Appendix E: Informative Annex contains a listing of industry test flux product sources.

The flux to be used in preparing the standard copper wrapping wires (see 3.2.4) for tests C and C1 **shall be standard Flux #2.** ~~conform to J-STD-004, Type ROL1. This flux shall be restricted to the preparation of the standard wrapping wires and shall not be used in performing the solderability tests for any of the methods herein.~~

3.2.2.1 Flux Maintenance Standard activated rosin fluxes #1 and #2 **shall** be covered when not in use and discarded after eight hours or the flux **shall** be maintained to a specific gravity of 0.843 ± 0.005 at $25 \pm 2^\circ\text{C}$ [$77 \pm 3.6^\circ\text{F}$] and discarded after one week of use.

~~**3.2.3 Flux Removal** Material used for cleaning flux from leads and terminations before solderability evaluations **shall** be capable of removing visible flux residues (see 5.4). The cleaned surface **shall** exhibit no mechanical damage.~~

3.2.3 Standard Copper Wrapping Wires The standard wrapping wires specified in 4.2.3.2 **shall** be fabricated from type S, soft or drawn and annealed, uncoated in accordance with (CID) A-A-59551 and prepared per the following process.

The nominal diameter of the wrapping wires **shall** be 0.6 mm [0.023 in]. The preparation of the wrapping wires **shall** be as follows:

- a. Straighten and cut wires into convenient lengths (50 mm [1.9 in] minimum).
- b. Degrease by immersion in an appropriate cleaner (e.g., isopropyl alcohol) for two minutes.
- c. Clean in fluoroboric acid 10% HBF_4 (by volume), in water, for five minutes at room temperature with agitation. Use caution in handling.
- d. Rinse acid off as follows:
 1. Two nonheated water rinses (deionized or distilled).
 2. Two isopropyl alcohol rinses.
 3. Air dry.
- e. ~~Immerse in standard flux #2. flux J-STD-004, Type ROL1.~~
- f. Dip in molten solder for five seconds at $245 \pm 5^\circ\text{C}$ [$473 \pm 9^\circ\text{F}$] for tin/lead solder alloy. Dip in molten solder for five seconds at $255 \pm 5^\circ\text{C}$ [$491 \pm 9^\circ\text{F}$] for lead-free solder alloy.

To remove or dissolve the residual flux, wash or rinse per 3.2.3.

Standard wrapping wires will be stored in a clean, covered container if not used immediately. The usable life of the standard wrapping wires **shall** not exceed 30 days after coating.

3.2.4 Water The water to be used for steam conditioning purposes **shall** be distilled or deionized.

3.3 Equipment The following equipment applies to more than one of the solderability test methods shown in this standard. Equipment that is specific to any of the test methods is described in the specific Clause 4 paragraphs detailing the method.

3.3.1 Steam Preconditioning Apparatus The steam conditioning chamber **shall** be constructed of non-corrodible materials such as borosilicate glass, quartz glass, stainless steel or PTFE. The specimen holder **shall** be non-reactive to prevent galvanic corrosion. The container should be insulated. The steam temperature at the conditioning level **shall** be maintained per the requirements of Table 3-2.

Table 3-2 Steam Temperature Requirements

| Altitude | Average Local Boiling Point °C | Steam Temperature Limits °C |
|-------------|--------------------------------|-----------------------------|
| 0-305 m | 100 | 93 ± 3 |
| 305-610 m | 99 | 92 ± 3 |
| 610-914 m | 98 | 91 ± 3 |
| 914-1219 m | 97 | 90 ± 3 |
| 1219-1524 m | 96 | 89 ± 3 |
| 1524-1829 m | 95 | 88 ± 3 |

A safe means to prevent excessive pressure and a means of maintaining adequate water level **shall** be provided. Neither **shall** cause the vapor to cool below the specified temperature. Condensate **shall** drip freely back to the water. Care should be taken to minimize contact between the condensate and the specimens.

3.3.2 Bake Preconditioning Apparatus A bake oven of sufficient size and capable of continuously maintaining 150 °C ± 5 °C temperature control shall be used.

~~**3.3.2 Solder Vessel** A thermostatically controlled static solder vessel **shall** be used for all applicable tests. The solder vessel **shall** be of adequate dimensions to accommodate the specimens and contain sufficient solder to maintain the solder temperature during testing, and to prevent exceeding the contamination levels (see 3.5.1 and 3.5.2). A minimum of 750 grams of solder should be used. Note: Precautions should be taken to avoid solder vessel damage due to metal erosion when using Lead free solder alloys.~~

3.3.3 Optical Inspection Equipment All test methods requiring visual inspection **shall** use microscope(s) capable of 10X magnification (see individual test methods), equipped with reticles, or equivalent, for measurement. An example of a reticle is shown in Figure 3-1. Shadowless lighting (i.e. uniform, nonglare, nondirectional) **shall** be suitable for proper inspection.

3.3.3.1 Referee Magnification Referee magnification **shall** be 30X. For fine pitch leaded parts (0.5 mm [0.020 in] pitch or less) the referee magnification **shall** be 70X. Referee conditions **shall** only be used to accept a product that has been rejected at the inspection magnification.

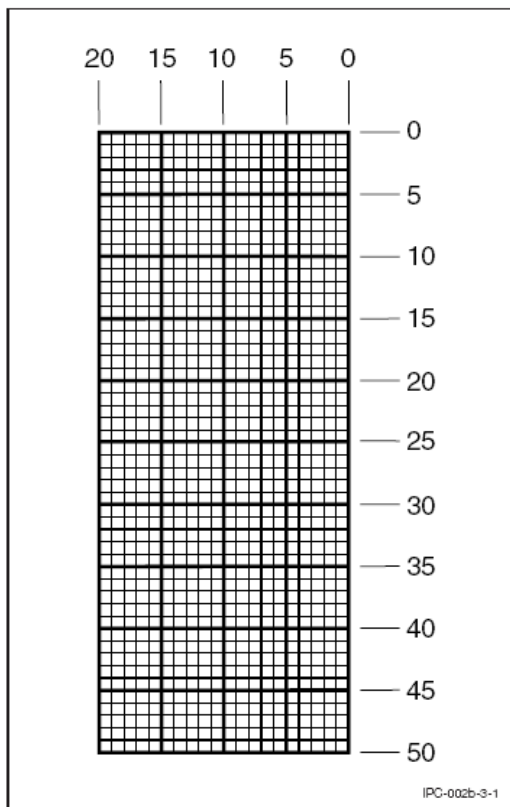


Figure 3-1 Example Reticle

3.3.4 Dipping Equipment Solder dipping devices **shall** be mechanical/electro-mechanical and capable of controlling the immersion (rates), dwell time and immersion depth as specified in 4.2.1 to 4.2.9. Sample holding fixtures **shall** be designed to avoid trapping any excess flux in the fixture and to minimize heat loss and assure reproducibility of test results.

3.3.5 Timing Equipment Timing equipment **shall** be automated, where applicable, and accurate to the limits of the test method.

3.4 Preparation for Testing

3.4.1 Specimen Preparation and Surface Condition All component leads or terminations **shall** be tested in the condition that they would normally be in at the time of assembly soldering. The specimen surfaces to be tested **shall** be handled in such a manner as to not cause contamination, nor **shall** the leads or terminations being tested be wiped, cleaned, scraped or abraded.

Special preparation of leads or terminations, such as bending or reorientation before test, **shall** be specified in the applicable procurement document. If the insulation on stranded wire must be removed, it **shall** be done in a manner so as not to loosen or damage the individual strands of the wires.

3.4.1.1 Steam-Preconditioning Categories The user **shall** specify to the vendor, as part of the purchase agreement, the required coating durability (see 1.4) and preconditioning category per Table 3-3. ~~Accelerated steam conditioning shall be performed per Table 1-1.~~ Solderability testing **shall** be performed per the appropriate test in Table 3-34.

Table 3-3 Preconditioning Parameters for Solderability Testing

| Condition Category | Precondition Type | Exposure Parameters | Use Guidelines |
|--------------------|-------------------|--|---|
| A | Steam | 1 Hour \pm 5 min. | Optional legacy condition for nontin and nontin-alloy lead finishes |
| B | | 4 Hours \pm 10 min. | Optional legacy condition |
| C | | 8 Hours \pm 15 min. | Optional legacy condition for tin and tin-alloy lead finishes |
| D | | 16 Hours \pm 30 min. | Optional legacy condition |
| E | 150 °C Dry Bake | 16 Hours \pm 30 min. | Alternative to steam precondition |
| E | | 4 Hours \pm 15 min. | Default Condition for tin and nontin alloy lead finishes |

3.4.2 Steam Conditioning Before the application of flux and subsequent solderability testing, **all specimens designated Condition Category A thru D shall** be conditioned in the device and under the conditions described in 3.3.1 at a steam temperature which is 7°C [12.6°F] below the local boiling point (see Table 3-2).

All components to be tested **shall** be placed into the steam conditioning chamber such that no specimens have their leads or terminations touching, and that condensation forming will drain away from the lead or terminations to the package body, e.g., “Dead Bug” for dual-inline packages.

Specimens **shall not** be stacked in a manner which restricts their surface exposure to steam nor **shall** they be placed closer than 10 mm [0.39 in] from the outer chamber walls, and **shall not** touch the inner container walls. In addition, no portion of the specimen **shall** be less than 40 mm [1.57 in] above the water level.

3.4.2.1 Post Steam Conditioning Drying After steam conditioning is complete, specimens **shall** be immediately removed from the chamber and dried using one of these two procedures: (1) ambient air for 15 minutes minimum; (2) bake at 100C maximum for no more than 1 hour in dry atmosphere (dry nitrogen recommended). Solderability testing **shall** be performed within 72 hours of removal from the chamber.

3.4.2.2 Steam Equipment Maintenance Before use, the steam conditioning apparatus **shall** have been cleaned with deionized or distilled water or hydrogen peroxide to remove any accumulated residues. This cleaning should be accomplished within five working days of the conditioning period.

Table 3-34 Solderability Test Selection Component Type

| Test Method | Wrapped Wires | Through-Hole Mount | Surface Mount | | |
|--|---------------|--------------------|---------------|----------|-----------|
| | | | Leadless | J-Lead | Gull Wing |
| Visual Acceptance Criteria Tests | | | | | |
| A – Dip & Look Test (Leaded Components) Tin/Lead Solder A1- Dip & Look Test (Leaded Components) Lead-free Solder | | X | | X | X |
| B – Dip & Look Test (Leadless Components) Tin/Lead Solder B1 – Dip & Look Test (Leadless Components) Lead-free Solder | | | X | | |
| C – Wrapped Wires Test Tin/Lead Solder C1 – Wrapped Wires Test Lead-free Solder | X | | | | |
| D – Resistance to Dissolution of Metallization Test (Tin/Lead and Lead-free Solder) | | | X | X | X |
| S – Surface Mount Process Simulation Test Tin/Lead Solder S1 – Surface Mount Process Simulation Test Lead-free Solder | | | X | X | X |
| Force Measurement Tests | | | | | |
| E – Wetting Balance Solder Pot Test (Leaded Components) Tin/Lead Solder E1 - Wetting Balance Solder Pot Test (Leaded Components) Lead-free solder | | X | | X | X |
| F – Wetting Balance Solder Pot Test (Leadless Components) Tin/Lead Solder F1 - Wetting Balance Solder Pot Test (Leadless Components) Lead-free solder | | | X | | |
| G – Wetting Balance Globule Test Tin/Lead Solder G1 - Wetting Balance Globule Test Lead-free solder | | | X | X | X |

3.4.3 Surfaces to be Tested The critical areas of leads or terminations intended to be soldered **shall** be evaluated for solderability per the test method (see appendix A). This **shall** include both the bottom termination and castellation on chip carriers and on all surfaces intended to be soldered on discrete devices. Through-hole leads that are tested by Method A **shall** have a 25 mm [0.98 in] portion, or the whole lead if less than 25 mm [0.98 in], evaluated for solderability (see 4.2.1.4). Test methods **shall** be selected per Table 3-34.

Surfaces to be tested by Method D **shall** be completely immersed in molten solder during dipping (see 4.2.4).

3.5 Solder Bath Requirements

~~**3.5.1 Solder Temperatures** Tin/lead plated thru hole technology solderability testing **shall** be done at a solder temperature of $245 \pm 5^\circ\text{C}$ [$473 \pm 9^\circ\text{F}$]. Tin/lead surface mount technology solderability testing **shall** be done at a solder temperature of $245 \pm 5^\circ\text{C}$ [$473 \pm 9^\circ\text{F}$] or $215 \pm 5^\circ\text{C}$ [$419 \pm 9^\circ\text{F}$] per agreed upon between user and vendor. Lead free solderability testing **shall** be done at a solder temperature of $255 \pm 5^\circ\text{C}$ [$491 \pm 9^\circ\text{F}$]. A temperature of $260 \pm 5^\circ\text{C}$ [$500 \pm 9^\circ\text{F}$] **shall** be used for Test Method D for both tin/lead or lead-free solder alloys.~~

3.5.1 Solder Contamination Control The solder in solder baths used for solderability testing **shall** be chemically or spectrographically analyzed or replaced each 30 operating days. The levels of contamination and Sn content must be within those shown in Table 3-45. The intervals between analysis may be lengthened if the test results indicate that the contamination limits are not being approached. The composition of the lead-free solder, including contamination levels, **shall** be maintained during testing with the silver and copper element levels adjusted for alloy requirements.

NOTE: An operating day consists of any eight-hour period, or any portion thereof, during which the solder is liquefied and used.

If contamination exceeds the limits specified in Table 3-45, then the solder **shall** be changed and the intervals between analyses **shall** be shortened. A sampling plan **shall** be developed, implemented, and documented, demonstrating solder contamination limit process control.

Table 3-45 Maximum Limits of Solder Bath Contaminant

| Contaminant | Maximum Contaminant Weight Percentage Limit Sn Pb Alloys ^(1,2) | Maximum Contamination Weight Percentage Limit Lead-free Alloys ^(3,4) |
|-------------|--|--|
| Copper | 0.300 | 1.000 |
| Gold | 0.200 | 0.200 |
| Cadmium | 0.005 | 0.005 |
| Zinc | 0.005 | 0.005 |
| Aluminum | 0.006 | 0.006 |
| Antimony | 0.500 | 0.500 |
| Iron | 0.020 | 0.020 |
| Arsenic | 0.030 | 0.030 |
| Bismuth | 0.250 | 0.250 |
| Silver | 0.100 | 4.000 |
| Nickel | 0.010 | 0.010 |
| Lead | N/A | 0.100 |

Notes:

1. The tin content of the solder **shall** be maintained within $\pm 1\%$ of the nominal alloy being used. Tin content **shall** be tested at the same frequency as testing for copper/gold contamination. The balance of the bath **shall** be lead and/or the items listed above.
2. The total of copper, gold, cadmium, zinc, and aluminum contaminants **shall** not exceed 0.4%. Not applicable to lead-free alloys.
3. The tin content of the solder **shall** be maintained within $\pm 1\%$ of the nominal alloy being used. Tin content **shall** be tested at the same frequency as testing for copper/silver concentration. The balance of the bath **shall** be the items listed above.
4. Maximum contamination limits are applicable for Sn96.5Ag3.0Cu0.5 (SAC305) per J-STD-006. Other Lead-free solder alloy contamination limits may be used upon agreement between user and vendor.

4 TEST PROCEDURES

4.1 Application of Flux Flux per 3.2.2 **shall** be used. Leads and terminations **shall** have flux applied uniformly and to cover the surfaces to be tested. The flux **shall** be at room temperature. This section, 4.1, **shall** apply to all of the following tests: A, B, C, D, E, F, G, A1, B1, C1, E1, F1 and G1 but **shall not** apply to both tests S and S1, which require the use of solder paste and not a separate flux.

Axial, radial, and multiple leaded components intended for through-hole mounting **shall** have their leads immersed into the flux approximately perpendicular to the flux surface. Leaded or leadless components intended for surface mounting **shall** have their leads or terminations immersed at an angle between 20° and 45° to the flux surface. The surfaces to be tested **shall** be immersed in the flux for 5 to 10 seconds. Any droplets of flux that may form **shall** be removed by blotting, taking care not to remove the flux coating from the surfaces to be tested. For small passive surface mount devices, the flux droplets may be (but are not required to be) blotted from the surface. The specimens being tested **shall** be allowed to dry for 5 to 20 seconds before solder immersion, but **shall** not be allowed to dwell above solder-pot (no preheat) before actual dipping action.

4.2 Visual Acceptance Criteria Tests

4.2.1 Test A -Tin/Lead Solder – Solder Bath/Dip and Look Test (Leads, Wires, etc.) This test is for solder bath/dip and look testing of leaded components, solid wires, and stranded wires greater than 0.254 mm [0.01 in] minimum.

4.2.1.1 Apparatus

4.2.1.1.1 Solder Pot/Bath A thermostatically controlled static solder vessel **shall** be used for all applicable tests. The solder vessel **shall** be of adequate dimensions to accommodate the specimens and contain sufficient solder to maintain the solder temperature during testing, and to prevent exceeding the contamination levels (see 3.5.1). A minimum of 750 grams of solder should be used.

4.2.1.1.2 Dipping Device A mechanical or electromechanical dipping device similar to the device shown in Figure 4-1 **shall** be used unless otherwise agreed to between user and vendor. The rate of immersion, dwell time, and rate of withdrawal **shall** be within the test limits defined in 4.2.1.3. Perpendicularity of through-hole component leads to solder surface **shall** be maintained. Leaded surface mount components **shall** be immersed at between 20° and 45° (or 90° if agreed upon) to the solder surface (see Figure 4-2). This angle **shall** remain consistent for any given component type. Wobble, vibration and other extraneous movements **shall** be minimized.

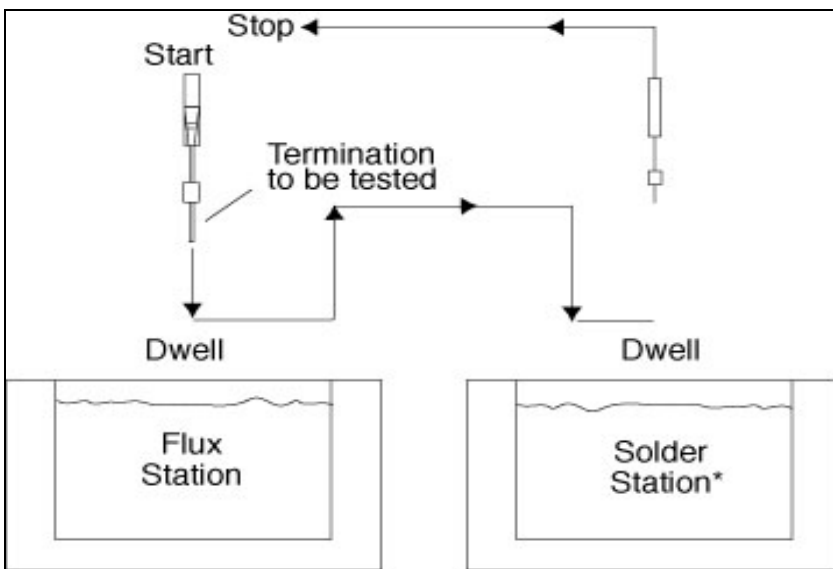


Figure 4-1 Dipping Schematic

***NOTE:** The solder temperature in the Solder Station **shall** be:
a) $245 \pm 5^\circ\text{C}$ [$473 \pm 9^\circ\text{F}$] for tin/lead solder testing, or
b) $255 \pm 5^\circ\text{C}$ [$491 \pm 9^\circ\text{F}$] for lead-free solder testing.

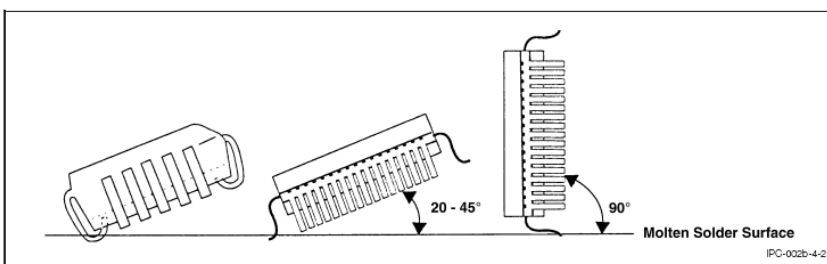


Figure 4-2 Solder Dipping Angle for Surface Mount Leaded Components

4.2.1.2 Preparation Specimen preparation and conditioning for testing **shall** be in accordance with 3.4.

4.2.2.3. Materials The solder **shall** meet the requirements of 3.2.1. The flux **shall** meet the requirements of 3.2.2.

4.2.1.4 Solder Temperatures Tin/lead solderability testing **shall** be done at a solder temperature of $245 \pm 5^\circ\text{C}$ [$473 \pm 9^\circ\text{F}$]. Alternatively, tin/lead surface mount technology component solderability testing **shall** be done at a solder temperature of $215 \pm 5^\circ\text{C}$ [$419 \pm 9^\circ\text{F}$] per agreed upon between user and vendor.

4.2.1.5 Procedure

- Dross and burned flux **shall** be skimmed from the surface of the molten solder immediately before dipping.
- The fluxed specimen **shall** be immersed in the molten solder to within 1.25 mm [0.049 in] of the component body or to the seating plane (whichever is further from the component body) for through-hole led components (see Figure 4-3).
- Immerse and withdraw at 25 ± 6 mm [0.984 ± 0.24 in] per second and dwell for $5 +0/-0.5$ seconds (see 5.2).
- After withdrawal, the solder **shall** be allowed to solidify by air cooling while the specimen is maintained in the test attitude.
- Before examination, all leads **shall** have all visible flux residues removed. The cleaned surface **shall** exhibit no mechanical damage.

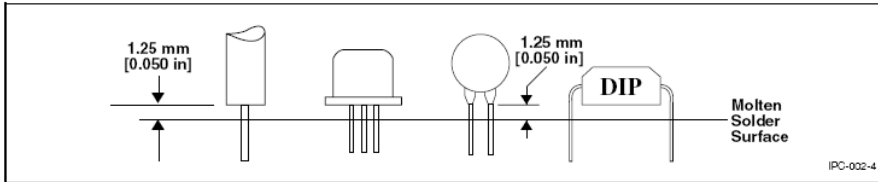


Figure 4-3 Solder Dipping Depth for Through-Hole Components

4.2.1.6 Evaluation

4.2.1.6.1 Magnification Parts **shall** be examined at 10X using the equipment specified in 3.3.3. For fine pitch leaded parts (0.5 mm [0.019 in] pitch or less) the inspection magnification **shall** be 30X.

4.2.1.6.2 Accept/Reject Criteria All leads **shall** exhibit a continuous solder coating free from defects for a minimum of 95% of the critical area of any individual lead. For exposed pad packages the exposed pad surfaces **shall** exhibit a continuous solder coating free from defects for a minimum of 80% of the critical area of those surfaces. Anomalies other than dewetting, nonwetting, and pin holes are not cause for rejection (see Appendices A and B). Exposed terminal metal is allowable on surface mount components at the toe end and on the vertical surfaces that are either unplated or sheared during component fabrication.

4.2.2 Test B - Tin/Lead Solder – Solder Bath/Dip and Look Test (Leadless Components) This test is for solder bath/dip and look testing of leadless components.

4.2.2.1 Apparatus

4.2.2.1.1 Solder Pot/Bath A thermostatically controlled static solder vessel **shall** be used for all applicable tests. The solder vessel **shall** be of adequate dimensions to accommodate the specimens and contain sufficient solder to maintain the solder temperature during testing, and to prevent exceeding the contamination levels (see 3.5.1). A minimum of 750 grams of solder should be used.

4.2.2.1.2 Vertical Dipping Device A mechanical or electro-mechanical dipping device similar to the device shown in Figure 4-1 **shall** be used unless otherwise agreed to between user and vendor. The rate of immersion, dwell time, and rate of withdrawal **shall** be within the test limits defined in 4.2.2.3. Surface mount leadless components **shall** be immersed at an angle to the solder surface of 20°- 45° and 90° for discrete chip components and exposed pad packages. Other immersion angles may be used if agreed upon between vendor and user.

4.2.2.2 Preparation Specimen preparation and conditioning for testing **shall** be in accordance with 3.4.

4.2.2.3. Materials The solder **shall** meet the requirements of 3.2.1. The flux **shall** meet the requirements of 3.2.2.

4.2.2.4 Solder Temperatures Tin/lead solderability testing **shall** be done at a solder temperature of $245 \pm 5^\circ\text{C}$ [$473 \pm 9^\circ\text{F}$]. Alternatively, tin/lead surface mount technology component solderability testing **shall** be done at a solder temperature of $215 \pm 5^\circ\text{C}$ [$419 \pm 9^\circ\text{F}$] per agreed upon between user and vendor.

4.2.2.5 Procedure

- Dross and burned flux **shall** be skimmed from the surface of the molten solder immediately before dipping.
- The fluxed specimen **shall** be immersed in the molten solder 0.10 mm [0.0039 in] minimum (see Figure 4-4). Immerse and withdraw at 25 ± 6 mm [0.984 ± 0.24 in] per second and dwell for 5 ± 0.5 seconds. Massive components may require a longer molten solder dwell time (see 5.2).
- After withdrawal, the solder **shall** be allowed to solidify by air cooling while the specimen is maintained in the test attitude.
- Before examination, all leads **shall** have all visible flux residues removed [per 3.2.3](#). The cleaned surface **shall** exhibit no mechanical damage.

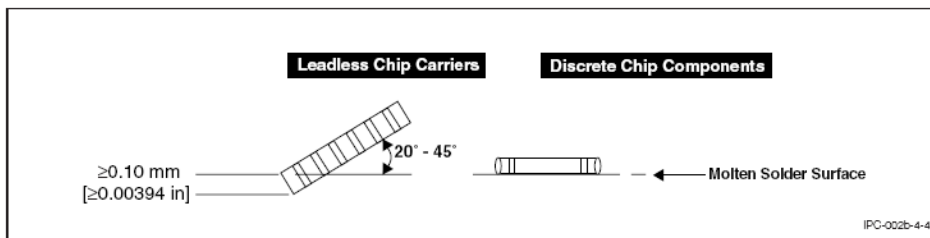


Figure 4-4 Leadless Component Immersion Depth

4.2.2.4 Evaluation

4.2.2.4.1 Magnification Parts **shall** be examined at 10X using the equipment specified in 3.3.3. For fine pitch termination parts (0.5 mm [0.020 in] pitch or less) the inspection magnification **shall** be 30X.

4.2.2.4.2 Accept/Reject Criteria All terminations **shall** exhibit a continuous solder coating free from defects for a minimum of 95% of the critical area of any individual termination. For exposed pad packages, the exposed pad surfaces shall exhibit a continuous solder coating free from defects for a minimum of 80% of the critical area of those surfaces. Anomalies other than dewetting, nonwetting, and pin holes are not cause for rejection (see Appendices A and B). Exposed terminal metal is allowable on surface mount components at the toe end and on the vertical surfaces that are either unplated or sheared during component fabrication.

4.2.3 Test C - Tin/Lead Solder – Wrapped Wires Test (Lugs, Tabs, Terminals, Large Stranded Wires) This test is for wrapped wires testing of lugs, tabs, terminals, stranded wires greater than 1.016 mm [0.040 in] diameter, and solid wires greater than 1.143 mm [0.045 in] diameter.

4.2.3.1 Apparatus

4.2.3.1.1 Solder Pot/Bath A thermostatically controlled static solder vessel **shall** be used for all applicable tests. The solder vessel **shall** be of adequate dimensions to accommodate the specimens and contain sufficient solder to maintain the solder temperature during testing, and to prevent exceeding the contamination levels (see 3.5.1). A minimum of 750 grams of solder should be used.

4.2.3.1.2 Dipping Device A mechanical or electromechanical dipping device similar to the device shown in Figure 4-1 **shall** be used unless otherwise agreed to between user and vendor. The rate of immersion, dwell time, and rate of withdrawal **shall** be within the test limits defined in 4.2.3.3. Wobble, vibration, and other extraneous movements **shall** be minimized.

4.2.3.2 Preparation Specimen preparation **shall** be in accordance with 3.4.

- For application of standard solderable wires for lugs, tabs, terminals, stranded wires greater than 1.016 mm [0.040 in] diameter, and solid wires greater than 1.143 mm [0.045 in] diameter all specimens **shall** have a wrap of 1.5 turns of the standard wires around the portion of the specimen to be tested.
- The standard wrapping wires as described in 3.2.4, **shall** be wrapped in such a manner so that it will not move during the solder dip. Examples of this wrap are shown in Figures 4-5 through 4-8.
- Special instructions concerning the portion of the specimens to be wrapped **shall** be specified in the individual specification, if necessary.
- For lugs and tabs designed to accept wires smaller than 0.6 mm [0.024 in] diameter, the standard copper wrapping wires specified in 3.2.4 **shall** be the same size for which the lugs and tabs are designed.

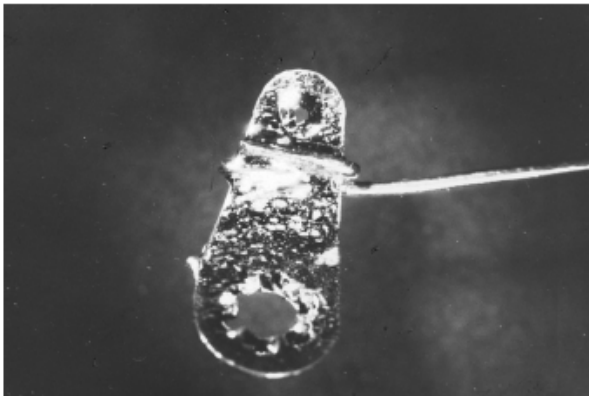


Figure 4-5 Illustration of Acceptable Solderable Terminal

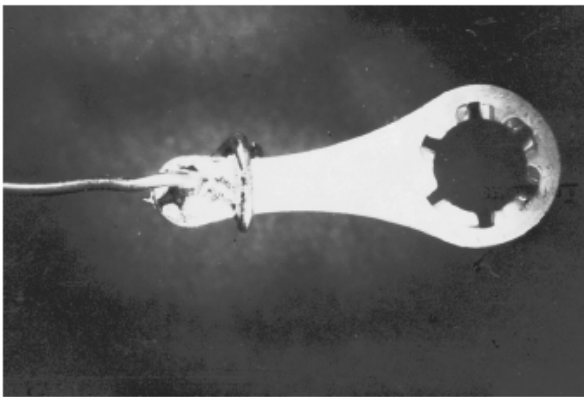


Figure 4-6 Illustration of Unsolderable Terminal

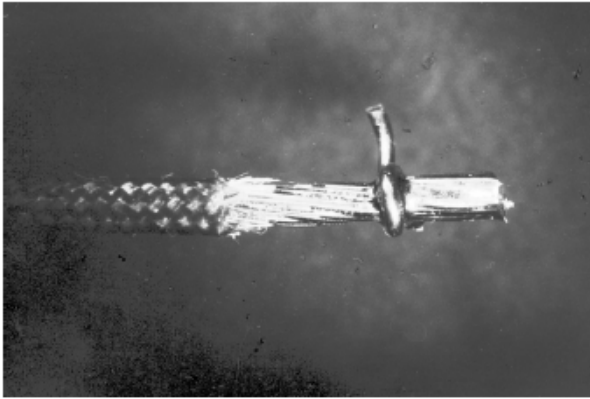


Figure 4-7 Illustration of Acceptable Solderable Stranded Wire

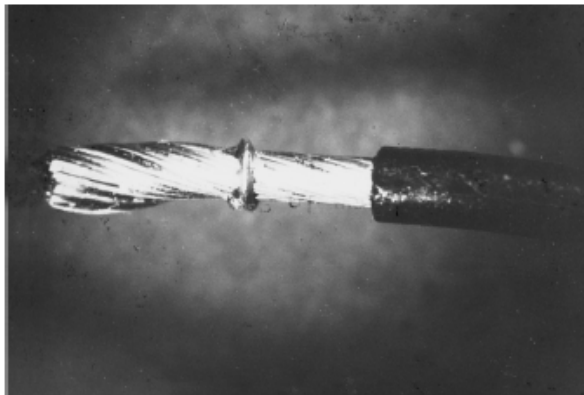


Figure 4-8 Illustration of Partially Solderable Stranded Wire Showing Incomplete Fillet

4.2.3.3 Materials The solder **shall** meet the requirements of 3.2.1. The flux **shall** meet the requirements of 3.2.2.

4.2.3.4 Solder Temperatures Tin/lead solderability testing **shall** be done at a solder temperature of $245 \pm 5^\circ\text{C}$ [$473 \pm 9^\circ\text{F}$].

4.2.3.5 Procedure

- The flux **shall** be at ambient (room) temperature.
- Terminations **shall** be immersed in the flux to the minimum depth necessary to cover the surface to be tested.
- The surface to be tested **shall** be immersed for 5 to 10 seconds and allowed to drain for 10 to 60 seconds.
- The dross and burned flux **shall** be skimmed from the surface of the molten solder just before immersing the terminations in the solder.
- Immerse and withdraw at a rate of 25 ± 6 mm [0.984 ± 0.24 in] per second and dwell for 7 ± 0.50 seconds.
- The part **shall** be attached to a dipping device and the flux-covered terminations immersed once in the molten solder to the same depth specified in 4.2.3.3b.
- After the dipping process, the part **shall** be allowed to cool in air.
- Before examination, all leads **shall** have all visible flux residues removed. The cleaned surface **shall** exhibit no mechanical damage.

4.2.3.6 Evaluation

4.2.3.6.1 Magnification Parts **shall** be examined at 10X using the equipment specified in 3.3.3.

4.2.3.6.2 Accept/Reject Criteria The criteria for acceptable solderability of lugs, tabs, terminals, stranded wires greater than 1.016 mm [0.040 in] diameter, solid wire greater than 1.143 mm [0.045 in] diameter are:

- A minimum of 95% of the total length of fillet between wrap wires and termination **shall** be tangent to the surface of the termination and be free of anomalies such as pinholes.
- A ragged or interrupted tangency line indicates a defect.

In case of dispute, the percent of fillet-length with defects **shall** be determined by their actual measurement. See Figure B-4 in Appendix B that serves as an aid in the evaluation of the 5% allowable defects.

4.2.4 Test D – Tin/Lead or Lead-Free Solder - Resistance to Dissolution of Metallization Test This test is to reveal a susceptibility to loss of solderability due to either:

- a. Dissolution of metallization over unsolderable base material (as indicated by loss of wetting), or
- b. Accumulation of impurities from the basis metal (as indicated by dewetting).

4.2.4.1 Apparatus

4.2.4.1.1 Solder Pot/Bath A thermostatically controlled static solder vessel **shall** be used for all applicable tests. The solder vessel **shall** be of adequate dimensions to accommodate the specimens and contain sufficient solder to maintain the solder temperature during testing, and to prevent exceeding the contamination levels (see 3.5.1). A minimum of 750 grams of solder should be used.

4.2.4.1.2 Dipping Device A mechanical or electromechanical dipping device similar to the device shown in Figure 4-1 **shall** be used unless otherwise agreed to between user and vendor. The rate of immersion, dwell time, and rate of withdrawal **shall** be within the test limits defined in 4.2.4.3.

4.2.4.1.3 Attitude (Angle of Immersion) All components **shall** be dipped using a vertical motion to ensure complete immersion of the surfaces to be soldered.

4.2.4.2 Preparation Specimen preparation **shall** be in accordance with 3.4.

4.2.4.3 Materials The solder **shall** meet the requirements of 3.2.1. The flux **shall** meet the requirements of 3.2.2.

4.2.4.4 Solder Temperatures A temperature of $260 \pm 5^{\circ}\text{C}$ [$500 \pm 9^{\circ}\text{F}$] **shall** be used for both tin/lead or lead-free solder alloys.

4.2.4.5 Procedure

- a. Dross and burned flux **shall** be skimmed from the surface of the molten solder immediately before dipping.
- b. The flux-covered component metallization **shall** be immersed only once in the molten solder to a minimum depth to completely cover the termination being tested.
- c. The angle of immersion **shall** be between 20° and 45° .
- d. Immerse and withdraw at a rate of 25 ± 6 mm [0.984 ± 0.24 in] per second and dwell for 30 ± 5 seconds. **After the dipping process, the part shall be allowed to cool in air.**
- h. Before examination, all leads **shall** have all visible flux residues removed. The cleaned surface **shall** exhibit no mechanical damage.

4.2.4.6 Evaluation

4.2.4.6.1 Magnification Parts **shall** be examined at 10X using the equipment specified in 3.3.3. For fine pitch components (0.5 mm [0.020 in] pitch or less) the inspection magnification **shall** be 30X.

4.2.4.6.2 Accept/Reject Criteria The criteria for acceptable resistance to leaching/dewetting **shall** be no more than 5% of the solderable metallization exhibiting exposed underlying, non-wettable base metal or metallization layers or portions of the ceramic substrate after exposure to molten solder.

4.2.5 Test S – Tin/Lead Solder – Surface Mount Process Simulation Test This test simulates actual surface mount component performance in a reflow process.

4.2.5.1 Apparatus

4.2.5.1.1 Stencil/Screen A stencil or screen with pad geometry openings in accordance with IPC 7351 shall be used. Alternative stencil pad geometry openings may be used upon agreement between vendor and user. Unless otherwise agreed upon between vendor and user, the nominal stencil thickness shall be per Table 4-1.

Table 4-1 Stencil Thickness Requirements

| Nominal Stencil Thickness | Component Lead Pitch |
|---------------------------|---------------------------------|
| 0.10 mm [0.00394 in] | <0.508 mm [<0.020 in] |
| 0.15 mm [0.00591 in] | 0.508-0.635 mm [0.020-0.025 in] |
| 0.20 mm [0.00787 in] | >0.635 mm [>0.025 in] |

4.2.5.1.2 Paste Application Tool A rubber or metal squeegee device shall be used to distribute paste across stencil/screen.

4.2.5.1.3 Test Substrate A ceramic substrate 0.635 mm [0.025 in] nominal thickness shall be used for testing. Other non-wettable substrates may be used if agreed upon between vendor and user.

4.2.5.1.4 Tin/lead Reflow Equipment An IR/convection reflow oven, vapor phase reflow system, or oven capable of reaching the reflow temperature of the tin/lead paste shall be used. Unless otherwise agreed upon between vendor and user the reflow parameters shall be per Table 4-2.

Table 4-2 Reflow Parameter Requirements

| | Temperature | Time |
|----------------------|-------------------------------|---------------------------------------|
| Vapor Phase Reflow | 215-219°C [419-426°F] | 30-60 seconds dwell at reflow |
| IR/Convection Reflow | 150-170°C [302-338°F] Preheat | 50-70 seconds |
| | 215-230°C [419-446°F] Reflow | 50-70 seconds |
| Oven | 215-230°C [419-446°F] | 2-5 minutes (until reflow is assured) |

Note: Table 4-2 reflow parameter values are for solderability testing purposes and are not related to moisture sensitivity level reflow test parameters.

4.2.5.2 Preparation Specimen preparation shall be in accordance with 3.4.

4.2.5.3 Procedure

- Place solder paste (per 3.2.1) onto stencil/screen and print the terminal pattern onto the test substrate by wiping paste over the stencil/screen in one smooth motion using rubber or metal squeegee.
- Remove the stencil/screen carefully to avoid smearing the paste print.
- Verify a paste print equivalent in geometry to the terminal of the device to be tested.
- Place the terminals of the component being tested on the solder paste print.
- Verify component placement by appropriate magnification.
- Place test substrate on applicable reflow equipment and conduct reflow process.
- After reflow, carefully remove substrate with component(s) and allow to cool to room temperature.
- Remove component(s) from substrate. Component leads may adhere slightly to substrate due to flux residue.
- Before examination, all leads shall have all visible flux residues removed. Care should be exercised in flux residue removal process to not damage leads.

4.2.5.4 Evaluation

4.2.5.4.1 Magnification Parts shall be examined at 10X using the equipment specified in 3.3.3. For fine pitch leaded/termination parts (0.5 mm [0.020 in] pitch or less) the inspection magnification shall be 30X.

4.2.5.4.2 Accept/Reject Criteria All leads shall exhibit a continuous solder coating free from defects for a minimum of 95% of the critical area of any individual lead. For exposed pad packages the exposed pad surfaces shall exhibit a continuous solder coating free from defects for a minimum of 80% of the critical area of those surfaces. Anomalies other than dewetting, nonwetting, and pin holes are not cause for rejection (see Appendices A and B). Exposed terminal metal is allowable on surface mount components at the toe end and on the vertical surfaces that are either unplated or sheared during component fabrication.

4.2.6 Test A1 - Lead-free Solder – Solder Bath/Dip and Look Test (Leads, Wires, etc.) This test is for solder bath/dip and look testing of leaded components, solid wires, and stranded wires greater than 0.254 mm [0.01 in] minimum.

4.2.6.1 Apparatus

4.2.6.1.1 Solder Pot/Bath A thermostatically controlled static solder vessel **shall** be used for all applicable tests. The solder vessel **shall** be of adequate dimensions to accommodate the specimens and contain sufficient solder to maintain the solder temperature during testing, and to prevent exceeding the contamination levels (see 3.5.1). A minimum of 750 grams of solder should be used.

4.2.6.1.2 Dipping Device A mechanical or electromechanical dipping device similar to the device shown in Figure 4-1 **shall** be used unless otherwise agreed to between user and vendor. The rate of immersion, dwell time, and rate of withdrawal **shall** be within the test limits defined in 4.2.6.3. Perpendicularity of through-hole component leads to solder surface **shall** be maintained. Leaded surface mount components **shall** be immersed at between 20° and 45° (or 90° if agreed upon) to the solder surface (see Figure 4-2). This angle **shall** remain consistent for any given component type. Wobble, vibration and other extraneous movements **shall** be minimized.

4.2.6.2 Preparation Specimen preparation and conditioning for testing **shall** be in accordance with 3.4.

4.2.6.3. Materials The solder **shall** meet the requirements of 3.2.1. The flux **shall** meet the requirements of 3.2.2.

4.2.6.4 Solder Temperature Lead-free solderability testing **shall** be done at a solder temperature of $245 \pm 5^\circ\text{C}$ [$473 \pm 9^\circ\text{F}$].

4.2.6.5 Procedure

- a. Dross and burned flux **shall** be skimmed from the surface of the molten solder immediately before dipping.
- b. The fluxed specimen **shall** be immersed in the molten solder to within 1.25 mm [0.049 in] of the component body or to the seating plane (whichever is further from the component body) for through-hole leaded components (see Figure 4-3).
- c. Immerse and withdraw at 25 ± 6 mm [0.984 ± 0.24 in] per second and dwell for $5 +0/-0.5$ seconds (see 5.2).
- d. After withdrawal, the solder **shall** be allowed to solidify by air cooling while the specimen is maintained in the test attitude.
- e. Before examination, all leads **shall** have all visible flux residues removed. The cleaned surface **shall** exhibit no mechanical damage.

4.2.6.6 Evaluation

4.2.6.6.1 Magnification Parts **shall** be examined at 10X using the equipment specified in 3.3.3. For fine pitch leaded parts (0.5 mm [0.019 in] pitch or less) the inspection magnification **shall** be 30X.

4.2.6.6.2 Accept/Reject Criteria All leads **shall** exhibit a continuous solder coating free from defects for a minimum of 95% of the critical area of any individual lead. For exposed pad packages the exposed pad surfaces shall exhibit a continuous solder coating free from defects for a minimum of 80% of the critical area of those surfaces. Anomalies other than dewetting, nonwetting, and pin holes are not cause for rejection (see Appendices A and B). Exposed terminal metal is allowable on surface mount components at the toe end and on the vertical surfaces that are either unplated or sheared during component fabrication.

4.2.7 Test B1 - Lead-free Solder – Solder Bath/Dip and Look Test (Leadless Components) This test is for solder bath/dip and look testing of leadless components.

4.2.7.1 Apparatus

4.2.7.1.1 Solder Pot/Bath A thermostatically controlled static solder vessel **shall** be used for all applicable tests. The solder vessel **shall** be of adequate dimensions to accommodate the specimens and contain sufficient solder to maintain the solder temperature during testing, and to prevent exceeding the contamination levels (see 3.5.1). A minimum of 750 grams of solder should be used.

4.2.7.1.2 Vertical Dipping Device A mechanical or electro-mechanical dipping device similar to the device shown in Figure 4-1 **shall** be used unless otherwise agreed to between user and vendor. The rate of immersion, dwell time, and rate of withdrawal **shall** be within the test limits defined in 4.2.7.3. Surface mount leadless components **shall** be immersed at an angle to the solder surface of 20°- 45° and 90° for discrete chip components and exposed pad packages. Other immersion angles may be used if agreed upon between vendor and user.

4.2.7.2 Preparation Specimen preparation and conditioning for testing **shall** be in accordance with 3.4.

4.2.7.3. Materials The solder **shall** meet the requirements of 3.2.1. The flux **shall** meet the requirements of 3.2.2.

4.2.7.4 Solder Temperature Lead-free solderability testing **shall** be done at a solder temperature of $245 \pm 5^{\circ}\text{C}$ [$473 \pm 9^{\circ}\text{F}$].

4.2.7.5 Procedure

- a. Dross and burned flux **shall** be skimmed from the surface of the molten solder immediately before dipping.
- b. The fluxed specimen **shall** be immersed in the molten solder 0.10 mm [0.0039 in] minimum (see Figure 4-4). Immerse and withdraw at 25 ± 6 mm [0.984 ± 0.24 in] per second and dwell for $5 +0/-0.5$ seconds. Massive components may require a longer molten solder dwell time (see 5.2).
- c. After withdrawal, the solder **shall** be allowed to solidify by air cooling while the specimen is maintained in the test attitude.
- d. Before examination, all leads **shall** have all visible flux residues removed. The cleaned surface **shall** exhibit no mechanical damage.

4.2.7.6 Evaluation

4.2.7.6.1 Magnification Parts **shall** be examined at 10X using the equipment specified in 3.3.3. For fine pitch termination parts (0.5 mm [0.020 in] pitch or less) the inspection magnification **shall** be 30X.

4.2.7.6.2 Accept/Reject Criteria All terminations **shall** exhibit a continuous solder coating free from defects for a minimum of 95% of the critical area of any individual termination. For exposed pad packages, the exposed pad surfaces **shall** exhibit a continuous solder coating free from defects for a minimum of 80% of the critical area of those surfaces. Anomalies other than dewetting, nonwetting, and pin holes are not cause for rejection (see Appendices A and B). Exposed terminal metal is allowable on surface mount components at the toe end and on the vertical surfaces that are either unplated or sheared during component fabrication.

4.2.8 Test C1 – Lead-free Solder - Wrapped Wires Test (Lugs, Tabs, Terminals, Large Stranded Wires) This test is for wrapped wires testing of lugs, tabs, terminals, stranded wires greater than 1.016 mm [0.040 in] diameter, and solid wires greater than 1.143 mm [0.045 in] diameter.

4.2.8.1 Apparatus

4.2.8.1.1 Solder Pot/Bath A thermostatically controlled static solder vessel **shall** be used for all applicable tests. The solder vessel **shall** be of adequate dimensions to accommodate the specimens and contain sufficient solder to maintain the solder temperature during testing, and to prevent exceeding the contamination levels (see 3.5.1). A minimum of 750 grams of solder should be used.

4.2.8.1.2 Dipping Device A mechanical or electromechanical dipping device similar to the device shown in Figure 4-1 **shall** be used unless otherwise agreed to between user and vendor. The rate of immersion, dwell time, and rate of withdrawal **shall** be within the test limits defined in 4.2.8.3. Wobble, vibration, and other extraneous movements **shall** be minimized.

4.2.8.2 Preparation Specimen preparation **shall** be in accordance with 3.4.

- For application of standard solderable wires for lugs, tabs, terminals, stranded wires greater than 1.016 mm [0.040 in] diameter, and solid wires greater than 1.15 mm [0.045 in] diameter, all specimens **shall** have a wrap of 1.5 turns of the standard wires around the portion of the specimen to be tested.
- The standard wrapping wires as described in 3.2.4 **shall** be wrapped in such a manner so that it will not move during the solder dip. Examples of this wrap are shown in Figures 4-5 through 4-8.
- Special instructions concerning the portion of the specimens to be wrapped **shall** be specified in the individual specification, if necessary.
- For lugs and tabs designed to accept wires smaller than 0.6 mm [0.024 in] diameter, the standard copper wrapping wires specified in 3.2.4 **shall** be the same size for which the lugs and tabs are designed.

4.2.8.3. Materials The solder **shall** meet the requirements of 3.2.1. The flux **shall** meet the requirements of 3.2.2.

4.2.8.4 Solder Temperature Lead-free solderability testing **shall** be done at a solder temperature of $245 \pm 5^\circ\text{C}$ [$473 \pm 9^\circ\text{F}$].

4.2.8.5 Procedure

- The flux **shall** be at ambient (room) temperature.
- Terminations **shall** be immersed in the flux to the minimum depth necessary to cover the surface to be tested.
- The surface to be tested **shall** be immersed for 5 to 10 seconds and allowed to drain for 10 to 60 seconds.
- The dross and burned flux **shall** be skimmed from the surface of the molten solder just before immersing the terminations in the solder.
- Immerse and withdraw at a rate of 25 ± 6 mm [0.984 ± 0.24 in] per second and dwell for 7 ± 0.50 seconds.
- The part **shall** be attached to a dipping device and the flux-covered terminations immersed once in the molten solder to the same depth specified in 4.2.8.3b.
- After the dipping process, the part **shall** be allowed to cool in air.
- Before examination, all leads **shall** have all visible flux residues removed. The cleaned surface **shall** exhibit no mechanical damage.

4.2.8.6 Evaluation

4.2.8.6.1 Magnification Parts **shall** be examined at 10X using the equipment specified in 3.3.3.

4.2.8.6.2 Accept/Reject Criteria The criteria for acceptable solderability of lugs, tabs, terminals, stranded wires greater than 1.016 mm [0.040 in] diameter, solid wires greater than 1.143 mm [0.045 in] diameter are:

- A minimum of 95% of the total length of fillet between wrap wires and termination **shall** be tangent to the surface of the termination and be free of anomalies such as pinholes.
- A ragged or interrupted tangency line indicates a defect.

In case of dispute, the percent of fillet-length with defects **shall** be determined by their actual measurement. See Figure B-4 in Appendix B that serves as an aid in the evaluation of the 5% allowable defects.

4.2.9 Test S1 – Lead-free Solder - Surface Mount Process Simulation Test This test simulates actual surface mount component performance in a reflow process.

4.2.9.1 Apparatus

4.2.9.1.1 Stencil/Screen A stencil or screen with pad geometry openings [in accordance with IPC 7351 shall be used](#). [Alternative stencil pad geometry openings may be used upon agreement between vendor and user](#). Unless otherwise agreed upon between vendor and user the nominal stencil thickness **shall** be per Table 4-3.

Table 4-3 Stencil Thickness Requirements

| Nominal Stencil Thickness | Component Lead Pitch |
|---------------------------|---------------------------------|
| 0.10 mm [0.00394 in] | <0.508 mm [<0.020 in] |
| 0.15 mm [0.00591 in] | 0.508-0.635 mm [0.020-0.025 in] |
| 0.20 mm [0.00787 in] | >0.635 mm [>0.025 in] |

4.2.9.1.2 Paste Application Tool A rubber or metal squeegee device **shall** be used to distribute paste across stencil/screen.

4.2.9.1.3 Test Substrate A ceramic substrate 0.635 mm [0.025 in] nominal thickness **shall** be used for testing. Other non-wettable substrates may be used if agreed upon between vendor and user.

4.2.9.1.4 Lead-Free Reflow Equipment An IR/convection reflow oven, vapor phase reflow system, or oven capable of reaching the reflow temperature of the lead-free paste **shall** be used. Unless otherwise agreed upon between vendor and user the reflow parameters **shall** be per Table 4-4.

Table 4-4 Lead-free Reflow Parameter Requirements

| | Temperature | Time |
|----------------------|-------------------------------|---------------------------------------|
| Vapor Phase Reflow | 217-240°C [423-464°F] | 45-90 seconds dwell at reflow |
| IR/Convection Reflow | 150-180°C [302-356°F] Preheat | 60-120 seconds |
| | 230-250°C [446-482°F] Reflow | 30-60 seconds |
| Oven | 230-250°C [446-482°F] | 2-5 minutes (until reflow is assured) |

Note: Table 4-4 reflow parameter values are for solderability testing purposes and are not related to moisture sensitivity level reflow test parameters.

4.2.9.2 Preparation Specimen preparation **shall** be in accordance with 3.4.

4.2.9.3 Procedure

- Place solder paste (see 3.2.1) onto stencil/screen and print the terminal pattern onto the test substrate by wiping paste over the stencil/screen in one smooth motion using rubber or metal squeegee.
- Remove the stencil/screen carefully to avoid smearing the paste print.
- Verify a paste print equivalent in geometry to the terminal of the device to be tested.
- Place the terminals of the component being tested on the solder paste print.
- Verify component placement by appropriate magnification.
- Place test substrate on applicable reflow equipment and conduct reflow process.
- After reflow, carefully remove substrate with component(s) and allow to cool to room temperature.
- Remove component(s) from substrate. Component leads may adhere slightly to substrate due to flux residue.
- [Before examination, all leads shall have all visible flux residues removed](#). Care should be exercised in flux residue removal process to not damage leads.

4.2.9.4 Evaluation

4.2.9.4.1 Magnification Parts **shall** be examined at 10X using the equipment specified in 3.3.3. For fine pitch leaded/termination parts (0.5 mm [0.020 in] pitch or less) the inspection magnification **shall** be 30X.

4.2.9.4.2 Accept/Reject Criteria All leads **shall** exhibit a continuous solder coating free from defects for a minimum of 95% of the critical area of any individual lead. For exposed pad packages, the exposed pad surfaces **shall** exhibit a continuous solder coating free from defects for a minimum of 80% of the critical area of those surfaces. Anomalies other than dewetting, nonwetting, and pin holes are not cause for rejection (see Appendices A and B). Exposed terminal metal is allowable on surface mount components at the toe end and on the vertical surfaces that are either unplated or sheared during component fabrication.

4.3 Force Measurement Tests

4.3.1 Test E – Tin/Lead Solder - Wetting Balance Solder Pot Test (Leaded Components) This test is for wetting balance testing of leaded components.

4.3.1.1 Apparatus A solder meniscus force measuring device (wetting balance) which includes a temperature controlled solder pot **shall** be used. The equipment **shall** have a means of recording force as a function of time, such as a chart recorder, data logger, or computer (see Figure 4-9).

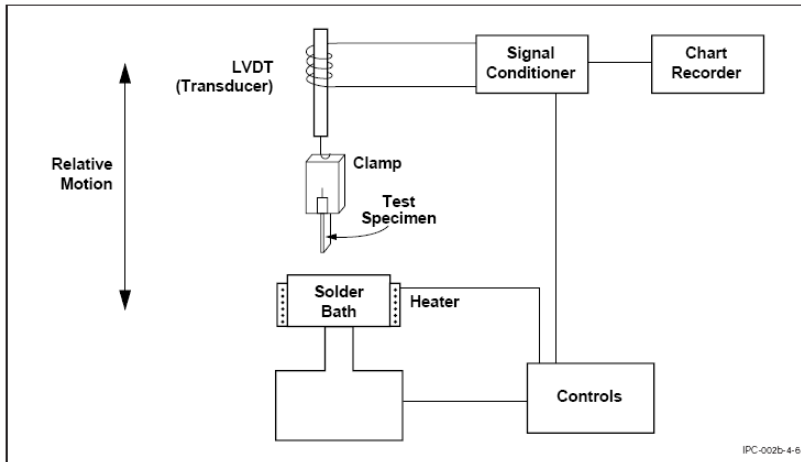


Figure 4-9 Wetting Balance Apparatus

4.3.1.1.1 Dipping Device A mechanical or electromechanical dipping device incorporated in the wetting balance **shall** be used. The device **shall** be preset to produce an immersion and emersion rate as specified in 4.3.1.3. The specimen dwell time is controlled to the time specified in 4.3.1.3. A device to sense contact of the lead(s) with the molten solder bath **shall** also be part of the fixture or instrument.

4.3.1.2 Preparation Specimen preparation and conditioning for testing **shall** be in accordance with 3.4.

4.3.1.3. Materials The solder **shall** meet the requirements of 3.2.1. The flux **shall** meet the requirements of 3.2.2. The solder contamination limits **shall** meet the requirements of 3.5.1.

4.3.1.4 Solder Temperature Lead-free solderability testing **shall** be done at a solder temperature of $245 \pm 5^\circ\text{C}$ [$473 \pm 9^\circ\text{F}$].

4.3.1.5 Procedure

- ~~Flux per 3.2.2 shall be used.~~ The flux **shall** be at ambient (room) temperature.
- Leads and terminations **shall** have flux applied uniformly and to cover the surfaces to be tested.
- Dross and burned flux **shall** be skimmed from the surface of the molten solder immediately before dipping.
- The flux covered termination **shall** be immersed only once in the molten solder to a **minimum** depth of 0.10 mm [0.0039 in].
- The angle of immersion **shall** be $20^\circ - 45^\circ$ (see Figure 4-2) or 0° , (see Figure 4-3), depending on the type of leaded component being tested.
- Immerse and withdraw at 1 mm - 5 mm [0.04 ± 0.20 in] per second and dwell for 5 ± 0.5 seconds. Massive components may require a longer solder dwell time (see 5.2).
- Before examination, all leads **shall** have all visible flux residues removed. The cleaned surface **shall** exhibit no mechanical damage.

4.3.1.6 Evaluation

4.3.1.6.1 Magnification Parts **shall** be examined at 10X using the equipment specified in 3.3.3. For fine pitch leaded/termination parts (0.5 mm [0.020 in] pitch or less) the inspection magnification **shall** be 30X.

4.3.1.6.2 Accept/Reject Criteria Suggested criteria for solderability evaluation for Test E are listed in Table 4-5. Figures 4-10 and 4-11 illustrate the suggested criteria of Table 4-5. In addition, the area of the test sample with fresh solder adhesion **shall** be greater than the area that was immersed in the solder bath (i.e., the component **shall** exhibit positive wicking beyond its immersion depth).

Table 4-5 Wetting Balance Parameter and Suggested Evaluation Criteria

| Parameter | Description | Suggested Criteria ¹ | |
|----------------|--|--|---|
| | | Set A | Set B |
| T ₀ | Time to buoyancy corrected zero | ≤1 second | ≤2 seconds |
| F2 | Wetting force at two seconds from start of test | 50% of maximum theoretical wetting force at or before two seconds ² | Positive value at or before two seconds |
| F5 | Wetting force at five seconds from start of test | No less than 90% of the F2 Value | No less than 90% of the F2 Value |
| AA | Integrated value of area of the wetting curve from start of test | Area calculated using sample buoyancy and 50% maximum theoretical force ³ | > zero (0) |

1. This suggested criteria **have** been established as a two-tier evaluation format with Set A being more stringent. Components meeting Set A suggested criteria are applicable to a larger soldering process window than components meeting Set B suggested criteria. It should be recognized that components meeting Set B suggested criteria may be completely acceptable to a larger process window but the user must determine which criteria set best integrates into their process.

2. See Appendix C for the method of calculating the maximum theoretical force.

3. See Appendix D for the method of calculation. (It is suggested that this method of calculation be programmed into the software used for control of the wetting balance test equipment.)

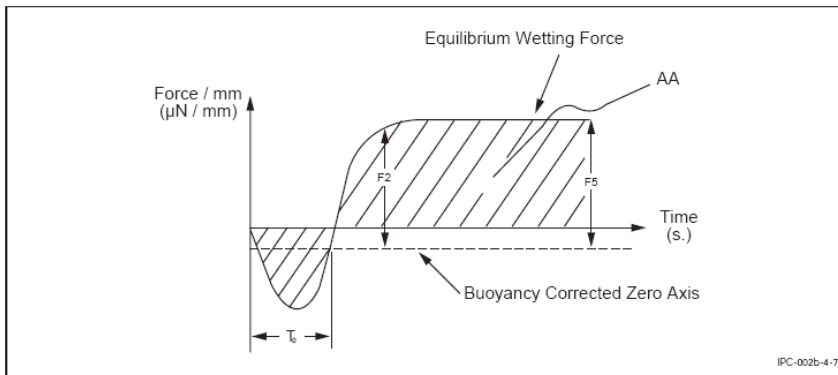


Figure 4-10 Set A Wetting Curve

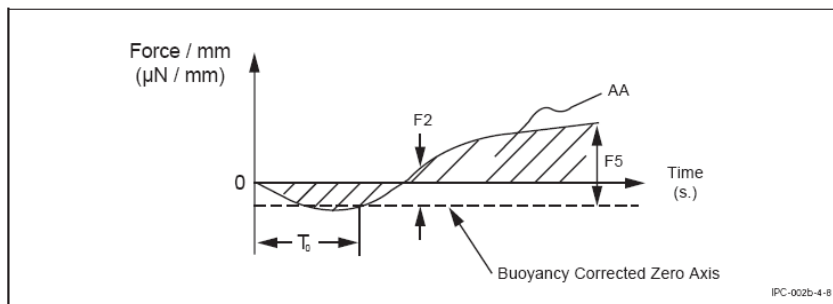


Figure 4-11 Set B Wetting Curve

4.3.2 Test F – Tin/Lead Solder - Wetting Balance Solder Pot Test (Leadless Components) This test is for wetting balance testing of leadless components.

4.3.2.1 Apparatus A solder meniscus force measuring device (wetting balance) which includes a temperature controlled solder pot **shall** be used. The equipment **shall** have a means of recording force as a function of time, such as a chart recorder, data logger, or computer (see Figure 4-9).

4.3.2.1.1 Dipping Device A mechanical or electromechanical dipping device incorporated in the wetting balance **shall** be used. The device **shall** be preset to produce an immersion and emersion rate as specified in 4.3.2.3. The specimen dwell time is controlled to the time specified in 4.3.2.3.

4.3.2.2 Preparation Specimen preparation and conditioning for testing **shall** be in accordance with 3.4.

4.3.2.3. Materials The solder **shall** meet the requirements of 3.2.1. The flux **shall** meet the requirements of 3.2.2. The solder contamination limits **shall** meet the requirements of 3.5.1.

4.3.2.4 Solder Temperature Lead-free solderability testing **shall** be done at a solder temperature of $245 \pm 5^{\circ}\text{C}$ [$473 \pm 9^{\circ}\text{F}$].

4.3.2.5 Procedure

- a. ~~Flux per 3.2.2 shall be used and~~ The flux **shall** be at ambient (room) temperature.
- b. Leads and terminations **shall** have flux applied uniformly and to cover the surfaces to be tested.
- c. Immerse and withdraw at 1 mm - 5 mm [0.04 in - 0.20 in] per second and dwell for 5 +0/-0.5 seconds. Massive components may require a longer solder dwell time (see 5.2).
- d. After application of the flux and post dip dwell, the specimen **shall** be mounted on the test equipment.
- e. Dross and burned flux **shall** be skimmed from the surface of the molten solder immediately before dipping.
- f. The flux covered termination **shall** be immersed only once in the molten solder to a depth of 0.10 mm [0.0039 in] minimum.
- g. The angle of immersion **shall** be per Figure 4-4.
- h. A full curve **shall** be recorded using the equipment specified in 4.3.2.1.
- i. Before examination, all leads **shall** have all visible flux residues removed. The cleaned surface **shall** exhibit no mechanical damage.

4.3.2.6 Evaluation

4.3.2.6.1 Magnification Parts **shall** be examined at 10X using the equipment specified in 3.3.3. For fine pitch termination parts (0.5 mm [0.020 in] pitch or less) the inspection magnification **shall** be 30X.

4.3.2.6.2 Accept/Reject Criteria Suggested criteria for solderability evaluation for Test F are listed in Table 4-5. Figures 4-10 and 4-11 illustrate the suggested criteria of Table 4-5. In addition, the area of the test sample with fresh solder adhesion **shall** be greater than the area that was immersed in the solder bath (i.e., the component **shall** exhibit positive wicking beyond its immersion depth).

4.3.3 Test G – Tin/Lead Solder - Wetting Balance Globule Test This test is for wetting balance globule testing of components.

4.3.3.1 Apparatus A solder meniscus force measuring device (wetting balance) which includes a temperature controlled vertical, cylindrical iron shaft shrunk fit into an aluminum housing on which is placed a specific sized piece of solder. Examples are: a 200 mg piece of solder for a 4 mm diameter globule block, a 100 mg piece of solder for a 3.2 mm diameter globule block, a 25 mg piece of solder for a 2 mm globule block or a 5 mg piece for a 1 mm globule block. The molten solder globule should ideally be replaced after each solderability test, but may be re-used for very small components that do not remove more than 1% of the solder volume per solder test for up to 5 tests. The equipment **shall** have a means of recording force as a function of time, such as a chart recorder, data logger or computer.

4.3.3.1.1 Dipping Device A mechanical or electromechanical dipping device incorporated in the wetting balance **shall** be used. The device **shall** be preset to produce an immersion and emersion rate as specified in 4.3.3.3.3. The specimen dwell time is controlled to the time specified in 4.3.3.3.3.

4.3.3.2 Materials

4.3.3.2.1 Flux The flux used **shall** be in accordance with 3.2.2.

4.3.3.2.2 Solder The solder **shall** be in accordance with 3.2.1. Other alloys may be used upon agreement between user and other parties.

4.3.3.2.3 Test Specimen The test specimen **shall** either be a full component or a lead that has been carefully removed from the component. Ideally the cross-section of the component to be dipped is either rectangular, square or round, to facilitate calculation of the theoretical maximum wetting force. Ideally no burr should be present but, if they are, generally they will be on the components used in production and should not be removed, as they may actually be the cause of the poor solderability. No cleaning of the specimen is allowed. Conditioning, if any, must be agreed to by the tester and other party in advance.

4.3.3.3 Procedure

4.3.3.3.1 Temperature of the Solder The temperature of the solder is to be stabilized at the required temperature for the test before commencing the test. [Tin/lead solderability testing shall be done at a solder temperature of \$245 \pm 5^{\circ}\text{C}\$ \[\$473 \pm 9^{\circ}\text{F}\$ \]. Alternatively, tin/lead surface mount technology component solderability testing shall be done at a solder temperature of \$215 \pm 5^{\circ}\text{C}\$ \[\$419 \pm 9^{\circ}\text{F}\$ \] per agreed upon between user and vendor.](#)

4.3.3.3.2 Fluxing A very small amount of flux is carefully applied to the surface or lead to be tested and the solder globule using a clean cotton tip or bud. No excess flux **shall** be ready to drip off the fluxed part or the excess flux must be drained off by carefully touching the lowest point on the surface to be tested with a piece of clean chemical lab filter paper. For this testing, ideally a small amount of flux should be kept in a small container that is only opened to immerse the cotton tip to wet it with flux. The cotton tips should be thrown away and replaced with fresh ones every 5 to 10 tests, with all tests done in the same testing interval. If testing is interrupted for more than a few minutes, then a new tip should be used.

4.3.3.3.3 Dipping Angle, Immersion Depth, and Immersion Rates The appropriate clip **shall** be chosen to hold the part as specified in Table 4-6 and illustrated in Figure 4-12. Without contaminating the surfaces to be tested, the specimen is mounted in the appropriate clip or other device supplied by the solderability tester manufacturer and carefully attached to the machine so as to not damage the transducer or dislodge the component from its orientation in the clip or other holder. The distance between the solderable surface of the sample and the solder globule should be fixed. An immersion speed between 1 mm/second and 5 mm/second [0.039 in/second and 0.20 in/second] should ensure that most test specimens are fully immersed. A dwell time of 5 seconds shall be used. A dwell time of 10 seconds may be necessary for large components or components with high thermal mass (see 5.2).

Table 4-6 Dipping Angle and Immersion Depth for Components (Directly from IEC 60068-2-69)

| Component ^a | | Dipping angle ^b | Figure (See Fig. 4-20) | Immersion depth (mm) | Pin size (mm) | Globule weight (mg) | Remarks |
|---------------------------|------------------------------------|----------------------------|------------------------|----------------------|---------------|---------------------|--|
| Capacitors | 1005 (0402) | Horizontal or Vertical | 2A, 2B | 0.10 | 2 | 25 | |
| | 1608 (0603) | | | | Horizontal | 2A | |
| | 2012 (0805) | 4 | 200 | | | | |
| | 3216 (1206) | | | | | | |
| Resistors | 1005 (0402) | Horizontal | 2B | 0.10 | 2 | 25 | |
| | 1608 (0603) | Horizontal or Vertical | 2A, 2B | | 3,2 or 4 | 100 or 200 | |
| | 2012 (0805) | | 2A,2B,2H ^c | | | | |
| | 3216 (1206) | | | | | | |
| Tantalum capacitors, LEDs | Case sizes A ^d ,B,C,D | Vertical | 2H ^c | 0.10 | 4 | 200 | |
| Leaded SMD | SOT 23, 25, 26, 323, 343, 353, 363 | 20 - 45 | 2D | 0.10 | 2 | 25 | 1 outer pin only |
| | SOT 89, | | 2F | 0.20 | 4 | 200 | |
| | SOT 223, 523 | | | 0.25 | | | |
| | Gull wing diode | | 2D | 0.20 | | | |
| | Any SOIC VSO QFP, SOP | | | | | | |
| | PLCC, SOJ | Horizontal | 2E | 0.10 | | | |
| | QFN | Horizontal | 2H ^c | 0.10 | 2 | 25 | Caution from bridging |
| | Cylindrical SMD | Horizontal or Vertical | 2A , 2B | 0.25 | 4 | 200 | |
| | SOD 80 | Vertical | 2B | 0.20 | 4 | 200 | |
| | Any BGA,CSP or LGA ^e | Horizontal | 2G | 0.10 | 2 | 25 | Only peripheral balls can be tested, and only test down to 1.0mm pitch |

Not recommended for sizes below 1005 (0402).

Bath method is preferred for capacitors 3216 (1206) size.

The recommended dwell time is 5s, except for SOT 89 and SOT 223 components where 10s is recommended.

For Figure 2B, rightward offset distance from the crest of the solder globule **shall** be 0% to 15% of the pin diameter and **shall** avoid leftward offset.

^a Component names in parentheses, dimensions are expressed in Imperial.

^b Orientation of the specimen terminals or leads toward the solder surface.

^c Figure 2H is applicable to the components which do not have electrode toward the solder surface when Figure 2B is applied.

^d This test may only be applicable with certain test equipment.

^e This test is recommended only for those balls and bumps that will not melt at the respective temperature and are not designed to melt during reflow operation.

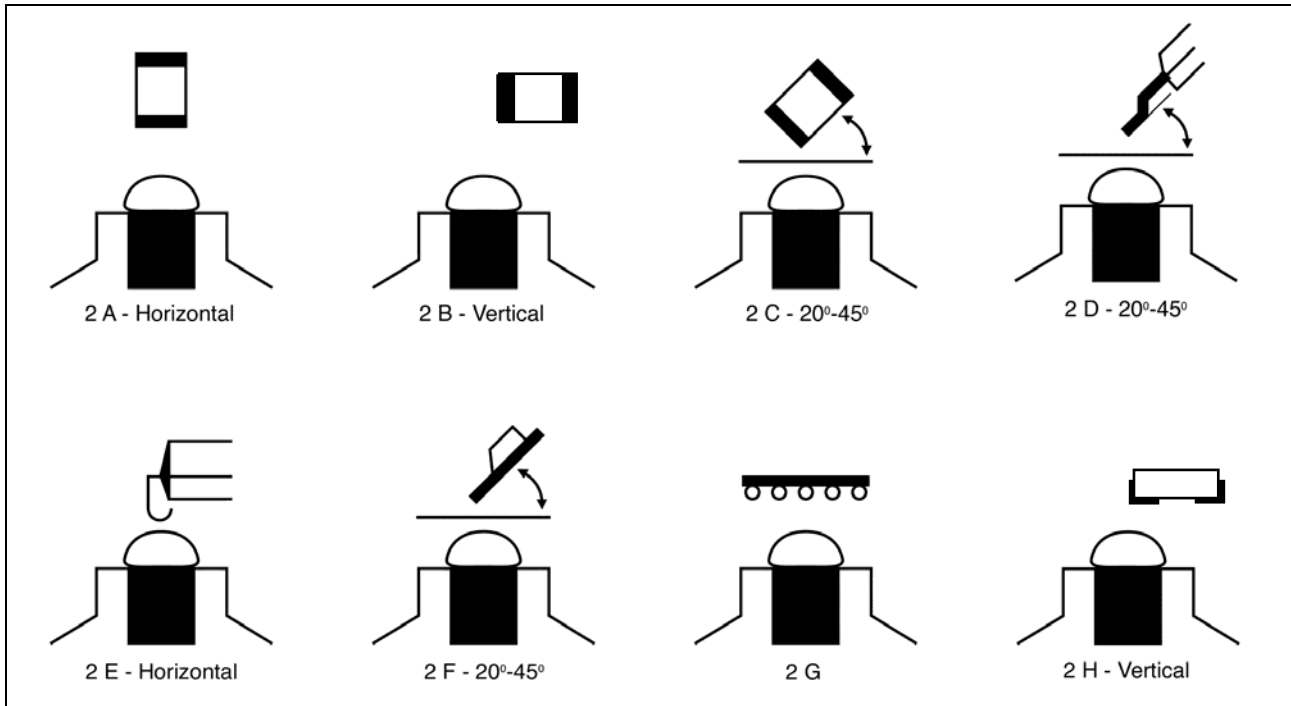


Figure 4-12 Component and Dipping Angle (Directly from IEC 60068-2-69)

4.3.3.3.4 Preheat The decision to use preheat should be by prior agreement between user and supplier (AABUS).

4.3.3.4 Evaluation

4.3.3.4.1 Magnification Magnifications of up to 100X may be necessary, for example, to examine components smaller than 0402 chip components.

4.3.3.4.2 Suggested Criteria Prior to any post-test examination, all specimens shall have the flux removed using a cleaning agent in accordance with 3.2.3. The area of the test sample with fresh solder adhesion shall be greater than the area that was immersed in the solder globule, (i.e. the component shall exhibit positive wetting beyond its immersion depth). In addition, Table 4-7 lists suggested criteria.

Table 4-7 Wetting Parameters and Suggested Evaluation Criteria

| Parameter | Description | Suggested Criteria | |
|----------------|--|---|---------------------------------------|
| | | Set A | Set B |
| T ₀ | Time to buoyancy corrected zero | ≤ 1 second | ≤ 2 seconds |
| F2 | Wetting force at 2 seconds from start of test | ≥50% maximum theoretical wetting force at or before 2 seconds | Positive value at or before 2 seconds |
| F5 | Wetting force at 5 seconds from start of test | At or above the positive value of F2 | At or above the value of F2 |
| AA | Integrated value of area of the wetting curve from start of the test | ≥ area calculated using sample buoyancy and 50% maximum theoretical force | > zero (0) |
| F2 | Wetting force at 2 seconds from start of test | ≥25% maximum theoretical wetting force at or before 2 seconds | |
| F5 | Wetting force at 5 seconds from start of test | At or above 90% of F2 value | |

4.3.4 Test E1 – Lead-free Solder - Wetting Balance Solder Pot Test (Leaded Components) This test is for wetting balance testing of leaded components.

4.3.4.1 Apparatus A solder meniscus force measuring device (wetting balance) which includes a temperature controlled solder pot **shall** be used. The equipment **shall** have a means of recording force as a function of time, such as a chart recorder, data logger, or computer (see Figure 4-9).

4.3.4.1.1 Dipping Device A mechanical or electromechanical dipping device incorporated in the wetting balance **shall** be used. The device **shall** be preset to produce an immersion and emersion rate as specified in 4.3.4.3. The specimen dwell time is controlled to the time specified in 4.3.4.3. A device to sense contact of the lead(s) with the molten solder bath **shall** also be part of the fixture or instrument.

4.3.4.2 Preparation Specimen preparation and conditioning for testing **shall** be in accordance with 3.4.

4.3.4.3. Materials The solder **shall** meet the requirements of 3.2.1. The flux **shall** meet the requirements of 3.2.2. The solder contamination limits **shall** meet the requirements of 3.5.1.

4.3.4.4 Solder Temperature Lead-free solderability testing **shall** be done at a solder temperature of $245 \pm 5^{\circ}\text{C}$ [$473 \pm 9^{\circ}\text{F}$].

4.3.4.5 Procedure

- a. The flux **shall** be at ambient (room) temperature.
- b. Leads and terminations **shall** have flux applied uniformly and to cover the surfaces to be tested.
- c. Dross and burned flux **shall** be skimmed from the surface of the molten solder immediately before dipping.
- d. The flux covered termination **shall** be immersed only once in the molten solder to a **minimum** depth of 0.10 mm [0.0039 in].
- e. The angle of immersion **shall** be 20° - 45° (see Figure 4-2) or 0° (see Figure 4-3), depending on the type of leaded component being tested.
- f. Immerse and withdraw at 1 mm - 5 mm [0.04 ± 0.20 in] per second and dwell for 5 +0/-0.5 seconds. Massive components may require a longer solder dwell time (see 5.2).
- g. Before examination, all leads **shall** have all visible flux residues removed. The cleaned surface **shall** exhibit no mechanical damage.

4.3.4.6 Evaluation

4.3.4.6.1 Magnification Parts **shall** be examined at 10X using the equipment specified in 3.3.3. For fine pitch leaded/termination parts (0.5 mm [0.020 in] pitch or less) the inspection magnification **shall** be 30X.

4.3.4.6.2 Accept/Reject Criteria Suggested criteria for solderability evaluation for Test E1 are listed in Table 4-5. Figures 4-10 and 4-11 illustrate the suggested criteria of Table 4-5. In addition, the area of the test sample with fresh solder adhesion **shall** be greater than the area that was immersed in the solder bath (i.e., the component **shall** exhibit positive wicking beyond its immersion depth).

4.3.5 Test F1 – Lead-free Solder - Wetting Balance Solder Pot Test (Leadless Components) This test is for wetting balance testing of leadless components.

4.3.5.1 Apparatus A solder meniscus force measuring device (wetting balance) which includes a temperature controlled solder pot **shall** be used. The equipment **shall** have a means of recording force as a function of time, such as a chart recorder, data logger, or computer (see Figure 4-9).

4.3.5.1.1 Dipping Device A mechanical or electromechanical dipping device incorporated in the wetting balance **shall** be used. The device **shall** be preset to produce an immersion and emersion rate as specified in 4.3.5.3. The specimen dwell time is controlled to the time specified in 4.3.5.3.

4.3.5.2 Preparation Specimen preparation and conditioning for testing **shall** be in accordance with 3.4.

4.3.5.3. Materials The solder **shall** meet the requirements of 3.2.1. The flux **shall** meet the requirements of 3.2.2. The solder contamination limits **shall** meet the requirements of 3.5.1.

4.3.5.4 Solder Temperature Lead-free solderability testing **shall** be done at a solder temperature of $245 \pm 5^{\circ}\text{C}$ [$473 \pm 9^{\circ}\text{F}$].

4.3.5.5 Procedure

- a. Flux **shall** be at ambient (room) temperature.
- b. Leads and terminations **shall** have flux applied uniformly and to cover the surfaces to be tested.
- c. Immerse and withdraw at 1 mm - 5 mm [0.04 in - 0.20 in] per second and dwell for 5 +0/-0.5 seconds. Massive components may require a longer solder dwell time (see 5.2).
- d. After application of the flux and post dip dwell, the specimen **shall** be mounted on the test equipment.
- e. Dross and burned flux **shall** be skimmed from the surface of the molten solder immediately before dipping.
- f. The flux covered termination **shall** be immersed only once in the molten solder to a depth of 0.10 mm [0.0039 in] minimum.
- g. The angle of immersion **shall** be per Figure 4-4.
- h. A full curve **shall** be recorded using the equipment specified in 4.3.5.1.
- i. Before examination, all leads **shall** have all visible flux residues removed. The cleaned surface **shall** exhibit no mechanical damage.

4.3.5.4 Evaluation

4.3.5.4.1 Magnification Parts **shall** be examined at 10X using the equipment specified in 3.3.3. For fine pitch termination parts (0.5 mm [0.020 in] pitch or less) the inspection magnification **shall** be 30X.

4.3.5.4.2 Accept/Reject Criteria Suggested criteria for solderability evaluation for Test F1 are listed in Table 4-5. Figures 4-10 and 4-11 illustrate the suggested criteria of Table 4-5. In addition, the area of the test sample with fresh solder adhesion **shall** be greater than the area that was immersed in the solder bath (i.e., the component **shall** exhibit positive wicking beyond its immersion depth).

4.3.6 Test G1 – Lead-free Solder - Wetting Balance Globule Test This test is for lead-free solder wetting balance globule testing of components.

4.3.6.1 Apparatus A solder meniscus force measuring device (wetting balance) which includes a temperature controlled vertical, cylindrical iron shaft shrunk fit into an aluminum housing on which is placed a specific sized piece of solder. Examples are: a 200 mg piece of solder for a 4 mm diameter globule block, a 25 mg piece for a 2 mm globule block or a 6.25 mg piece for a 1 mm globule block. The molten solder globule should ideally be replaced after each solderability test, but may be re-used for very small components that do not remove more than 1% of the solder volume per solder test for up to 5 tests. The equipment **shall** have a means of recording force as a function of time, such as a chart recorder, data logger or computer.

4.3.6.1.1 Dipping Device A mechanical or electromechanical dipping device incorporated in the wetting balance **shall** be used. The device **shall** be preset to produce an immersion and emersion rate as specified in 4.3.6.3.3. The specimen dwell time is controlled to the time specified in 4.3.6.3.3.

4.3.6.2 Materials

4.3.6.2.1 Flux The flux used shall be in accordance with 3.2.2.

4.3.6.2.2 Solder The solder alloy **shall** be in accordance with 3.2.1. Other alloys may be used upon agreement between user and other parties.

4.3.6.2.3 Test Specimen The test specimen shall either be a full component or a lead that has been carefully removed from the component. Ideally the cross-section of the component to be dipped is either rectangular, square or round, to facilitate calculation of the theoretical maximum wetting force. Ideally no burr should be present but, if they are, generally they will be on the components used in production and should not be removed, as they may actually be the cause of the poor solderability. No cleaning of the specimen is allowed. Conditioning, if any, must be agreed to by the tester and other party, in advance.

4.3.6.3 Procedure

4.3.6.3.1 Temperature of the Solder The temperature of the solder is to be stabilized at the required temperature for the test before commencing the test. **Lead-free solderability testing shall be done at a solder temperature of $245 \pm 5^{\circ}\text{C}$ [$473 \pm 9^{\circ}\text{F}$].**

4.3.6.3.2 Fluxing A very small amount of flux is carefully applied to the surface or lead to be tested and the solder globule using a clean cotton tip or bud. No excess flux **shall** be ready to drip off the fluxed part or the excess flux must be drained off by carefully touching the lowest point on the surface to be tested with a piece of clean chemical lab filter paper. For this testing, ideally a small amount of flux should be kept in a small container that is only opened to immerse the cotton tip to wet it with flux. The cotton tips should be thrown away and replaced with fresh ones every 5 to 10 tests, with all tests done in the same testing interval. If testing is interrupted for more than a few minutes, then a new tip should be used.

4.3.6.3.3 Dipping Angle, Immersion Depth, and Immersion Rates The appropriate clip **shall** be chosen to hold the part as specified in Table 4-6 and illustrated in Figure 4-12. Without contaminating the surfaces to be tested, the specimen is mounted in the appropriate clip or other device supplied by the solderability tester manufacturer and carefully attached to the machine so as to not damage the transducer or dislodge the component from its orientation in the clip or other holder. The distance between the solderable surface of the sample and the solder globule should be fixed. An immersion speed between 1 mm/second and 5 mm/second [0.039 in/second and 0.20 in/second] should ensure that most test specimens are fully immersed. A dwell time of 5 seconds **shall** be used. A dwell time of 10 seconds may be necessary for large components or components with high thermal mass (see 5.2).

4.3.6.3.4 Preheat The decision to use preheat should be as agreed upon between the user and supplier, prior to testing.

4.3.6.4 Evaluation

4.3.6.4.1 Magnification Magnifications of up to 100X may be necessary, for example, to examine components smaller than 0402 chip components.

4.3.6.4.2 Suggested Criteria Prior to any post-test examination, **all specimens shall have all visible flux residues removed. The cleaned surface shall exhibit no mechanical damage.** The area of the test sample with fresh solder adhesion **shall** be greater than the area that was immersed in the solder globule, (i.e. the component **shall** exhibit positive wetting beyond its immersion depth). In addition, Table 4-7 lists suggested criteria.

5 NOTES

5.1 Use of Activated Flux This standard specifies a rosin-based flux with a very specific quantity of activator. The intent of requiring the use of a specific quantity of flux activator is to reduce the variability of test results that were seen with pure rosin flux, enable the solderability testing of non-tin component lead metallizations, and provide a realistic solderability testing safety factor by keeping the amount of activator both fixed and less than that used for production soldering. The benefit of using this specified activated solderability testing flux composition was demonstrated by extensive testing, as reported in the J-STD-002B Activated Solderability Test Flux Rationale Committee Letter.

5.2 Massive Components Large components that have terminations with high heat sinking capacity may require longer dwell times to be applied to the dip test (for example: Tests A, B, C, E and F) to allow for the slower heat up time. In such cases an agreement to increase the dwell time will be required between the user and vendor. This agreement must also state the specific dwell time to be used.

5.3 Sampling Plans Sampling plans **shall** identify the number of components to be randomly selected from a given lot. All leads/terminations of the components selected **shall** be tested for solderability. Each lead of a component must pass for the component to pass. The selection and disposition of solderability test specimens **shall** be per the individual component specification. It may be necessary to bend back one out of every two or two out of every three leads for some test methods. These leads would obviously not be counted.

~~5.4 Safety Notes Isopropyl alcohol is flammable. Care must be taken in both usage and storage to keep the isopropyl alcohol from sparks or flames. See the Material Safety Data Sheets (MSDS) for all solvents. All chemicals shall be handled per appropriate data sheets, and disposed of per local regulations.~~

5.4 Correction for Buoyancy For the wetting balance to obtain wetting force values that are relatable to one another, it is necessary to correct for the variability in specimen sizes, in particular width and thickness. This is done by correcting for the volume of the sample immersed in the solder. The following formula may be used to calculate the buoyant force correction:

$$F_b = d (d_1 \text{ or } d_2) g_n V$$

where:

d_1 = Density of solder at 245°C (8150 kg/m³) for Sn60/Pb40 Alloy

d_2 = Density of solder at 255°C (7410 kg/m³) for SAC305 Alloy

g_n = Acceleration of gravity (9.810 m/s²)

V = Immersed volume in m³ (width x thickness x immersion depth)

The calculated buoyant force will be in Newtons and will be normalized for wetted perimeter and expressed as micro-Newtons/mm. As shown in Figures 4-18 and 4-19, using the convention that wetting force is positive upward, all measurements need to be buoyancy corrected for the times as well as the forces to be more accurate.

5.5 Accelerated Steam Conditioning Limitations The accelerated steam conditioning of solderable coatings has been, and continues to be, the subject of intense investigation (see IPC-TR-464). Compared to other conditioning methods, steam conditioning satisfactorily accelerates the degradation of tin and tin/lead surfaces in a manner similar to natural aging. The degradation mechanisms of surface oxidation and Cu/Sn intermetallic growth are both enhanced by the heat and humidity of steam. Properly applied tin and tin/lead coatings can withstand the steam conditioning environment well beyond the eight hours specified and may survive natural aging well beyond 12 months. Due to the combined effects of specific geometry, storage environment, and material systems, it is not possible to accurately predict storage life. ~~As a result, this specification indicates a storage life overlap for coating durability Category 1 and Category 3, and an open ended limit for coating durability Category 3. For coatings other than tin or tin-lead (Category 2), data do not exist to support steam conditioning longer than the one hour specified.~~

Appendix A
Critical Component Surfaces

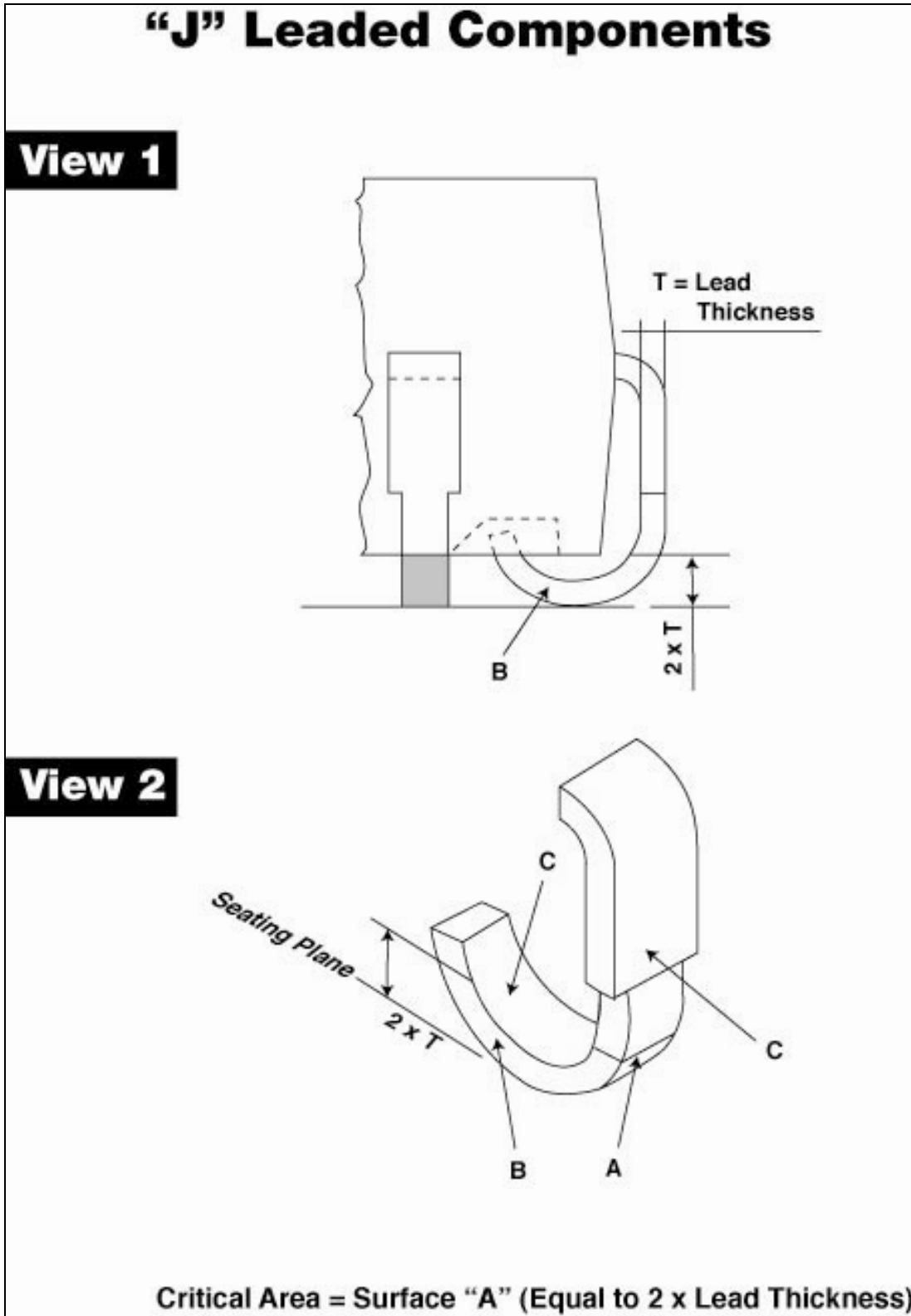
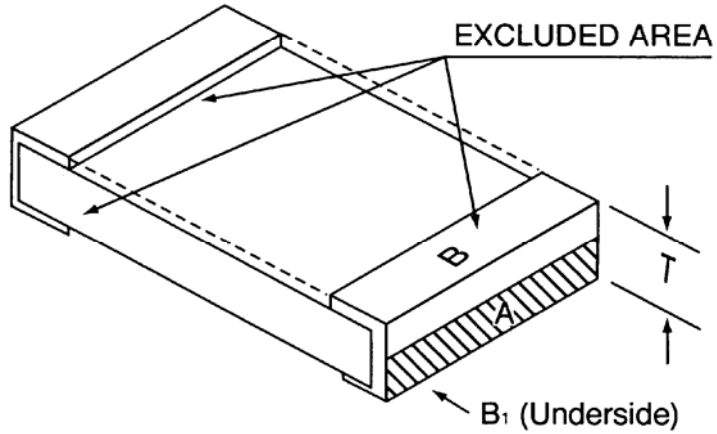


Figure A-1 “J” Leaded Components

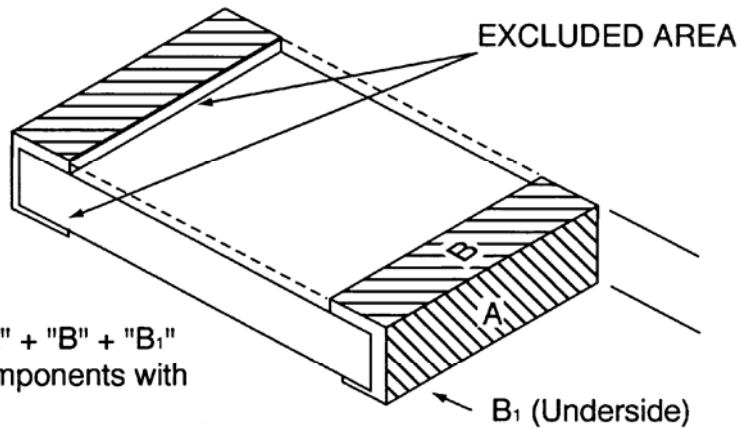
Passive Components

TESTS S and S1



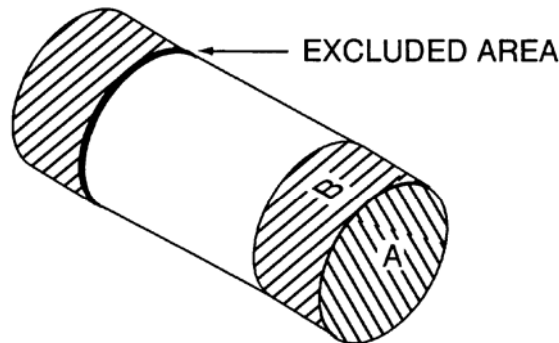
Critical Area = Surface "A" + "B₁" < 1/4 T or 0.5 mm whichever is less

TESTS B and B1



Critical Area = Surfaces "A" + "B" + "B₁"
Exclude surface "B" for components with "Top/Bottom" orientation

CYLINDRICAL



Critical Area = Entire end cap surface excluding inner rim

Figure A-2 Passive Components

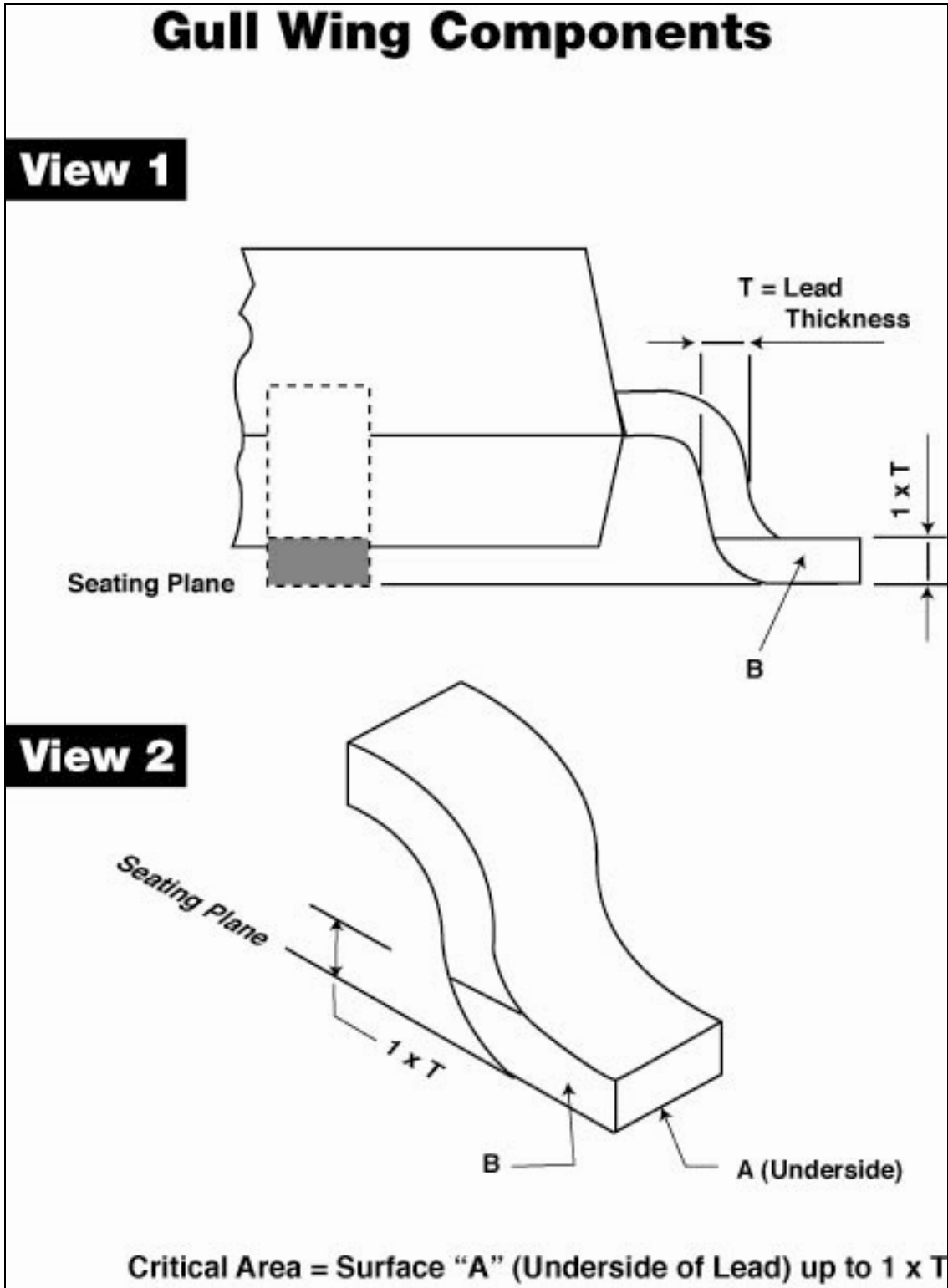


Figure A-3 Gull Wing Components

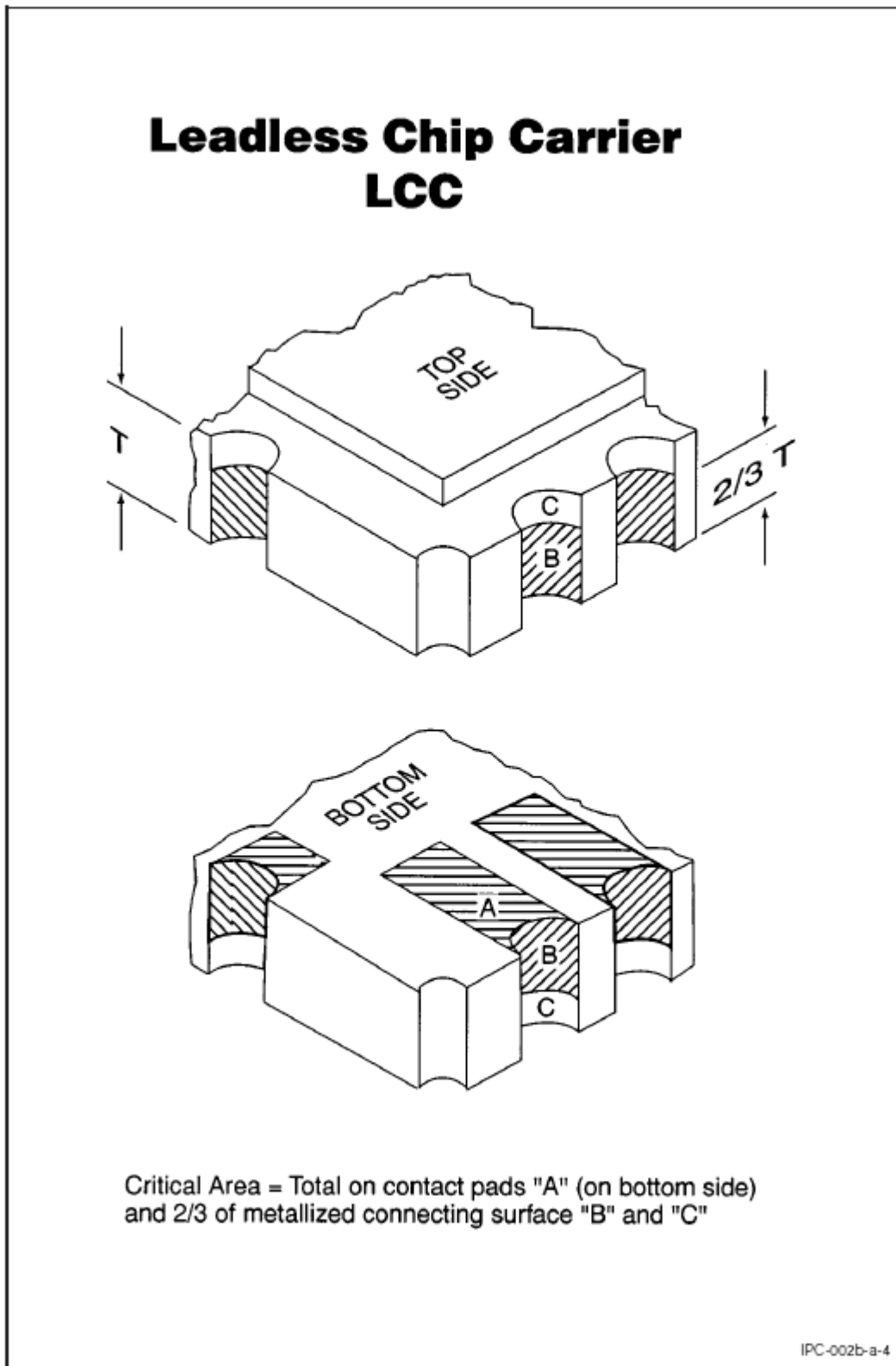


Figure A-4 Leadless Chip Carrier

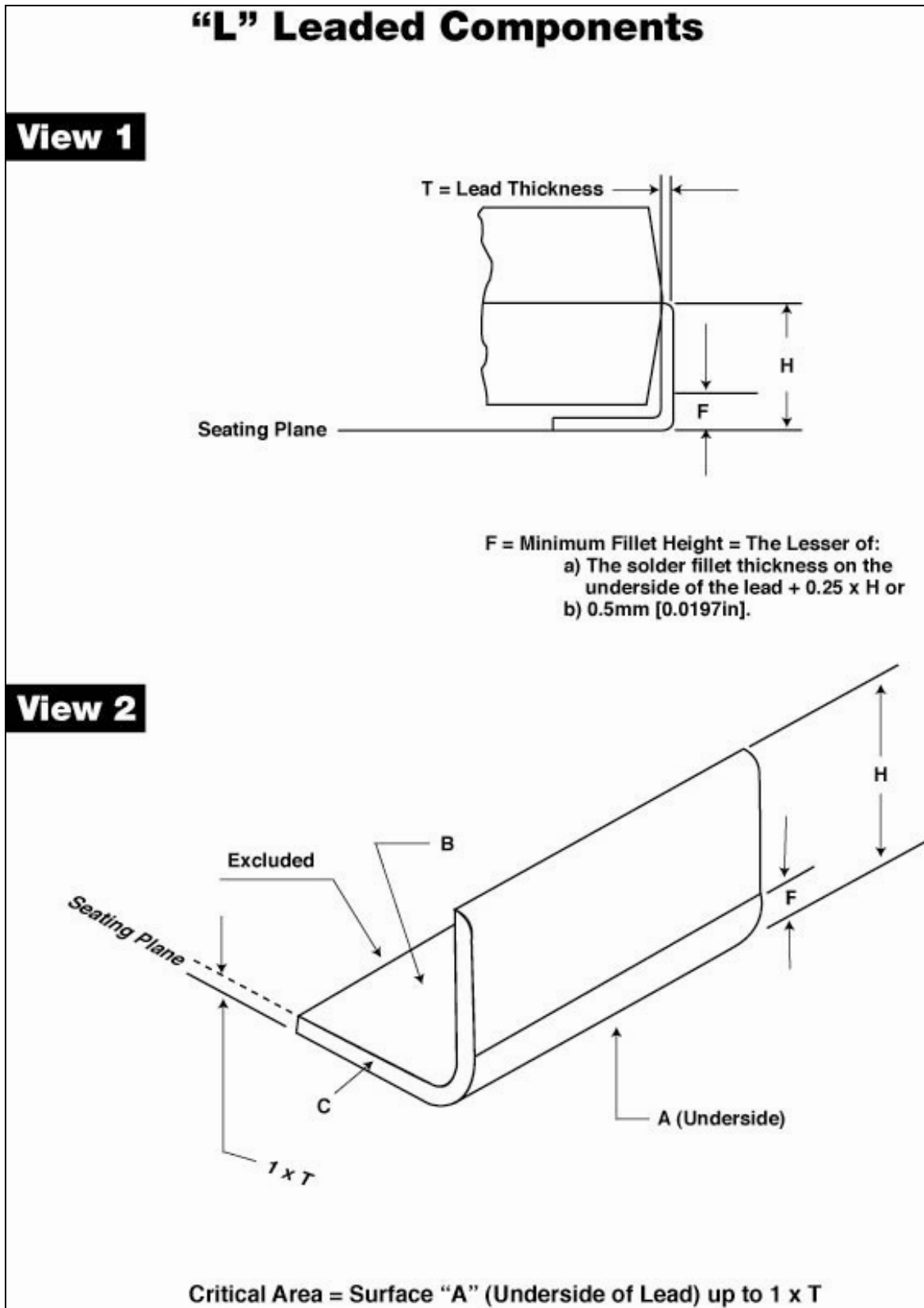


Figure A-5 “L” Leaded Components

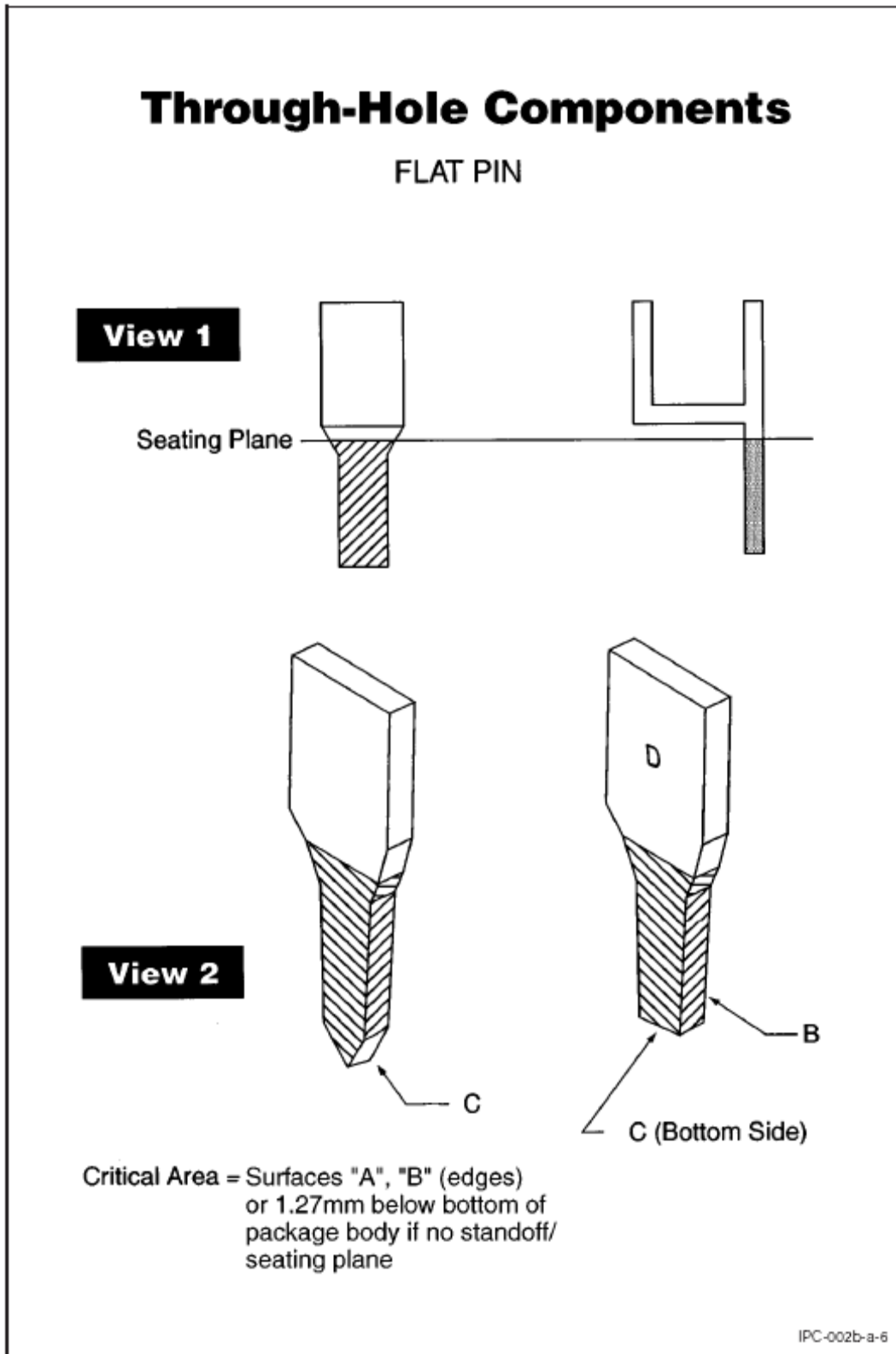
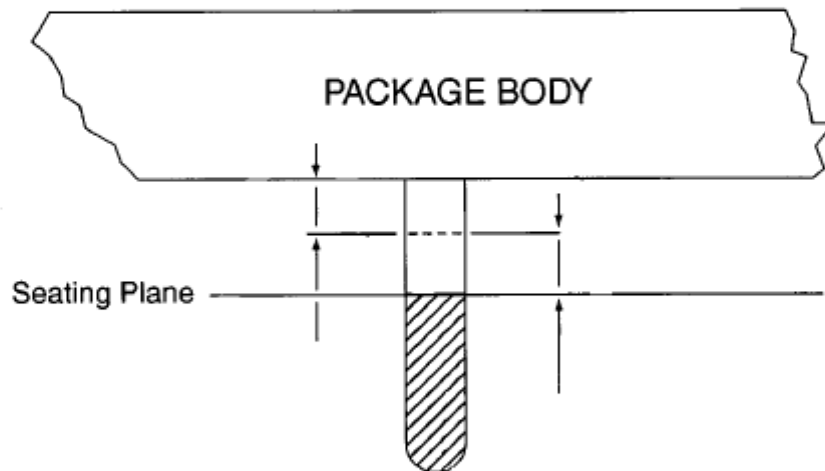


Figure A-6 Through-Hole Components – Flat Pin

Through-Hole Components

ROUND PIN



Critical Area = Entire surface of pin plus .75mm above seating plane or 1.27mm below bottom of package body whichever is the smallest distance

IPC-002b-a-7

Figure A-7 Through-Hole Components – Round Pin

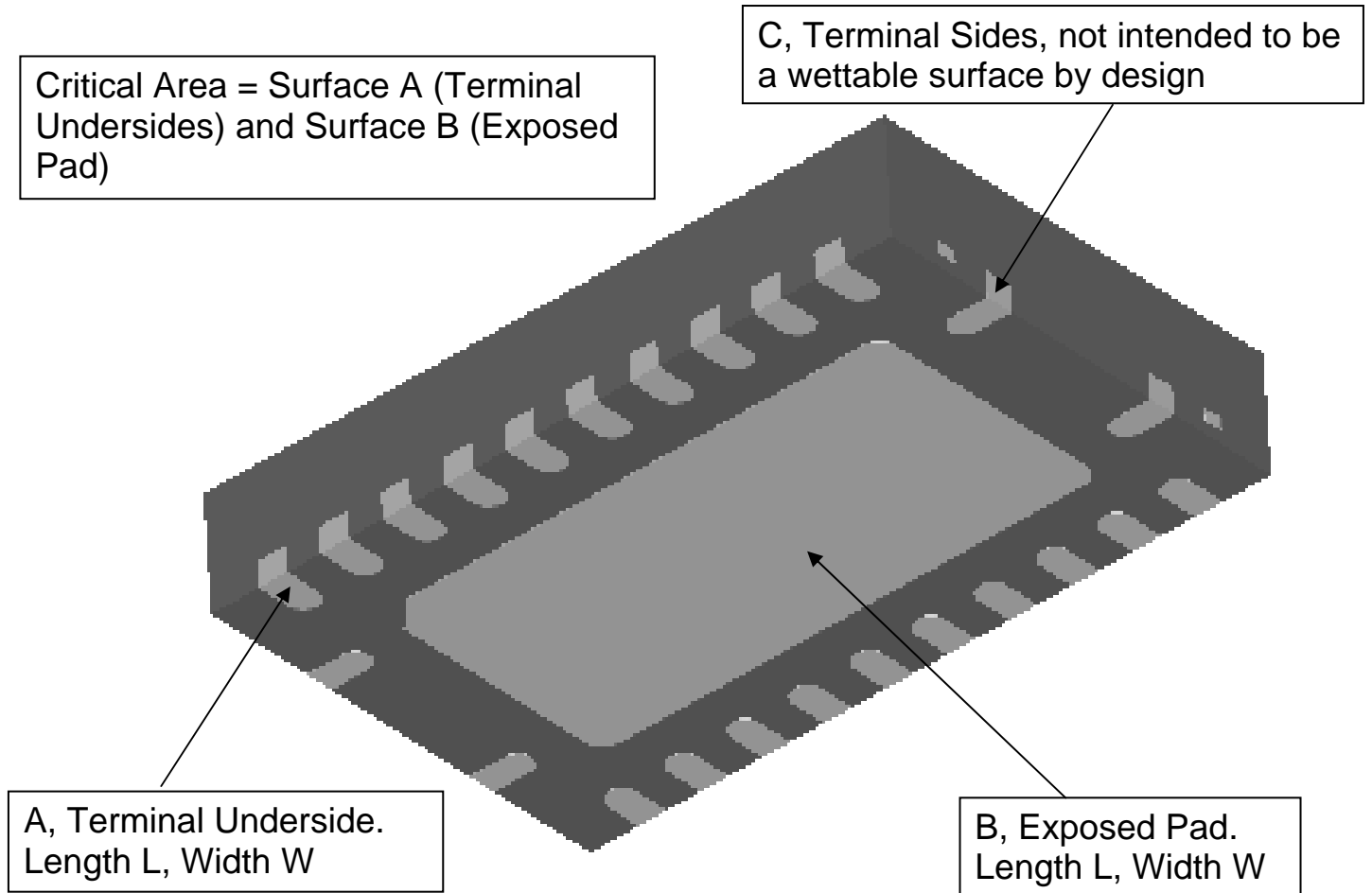


Figure A-8 Exposed Pad Package

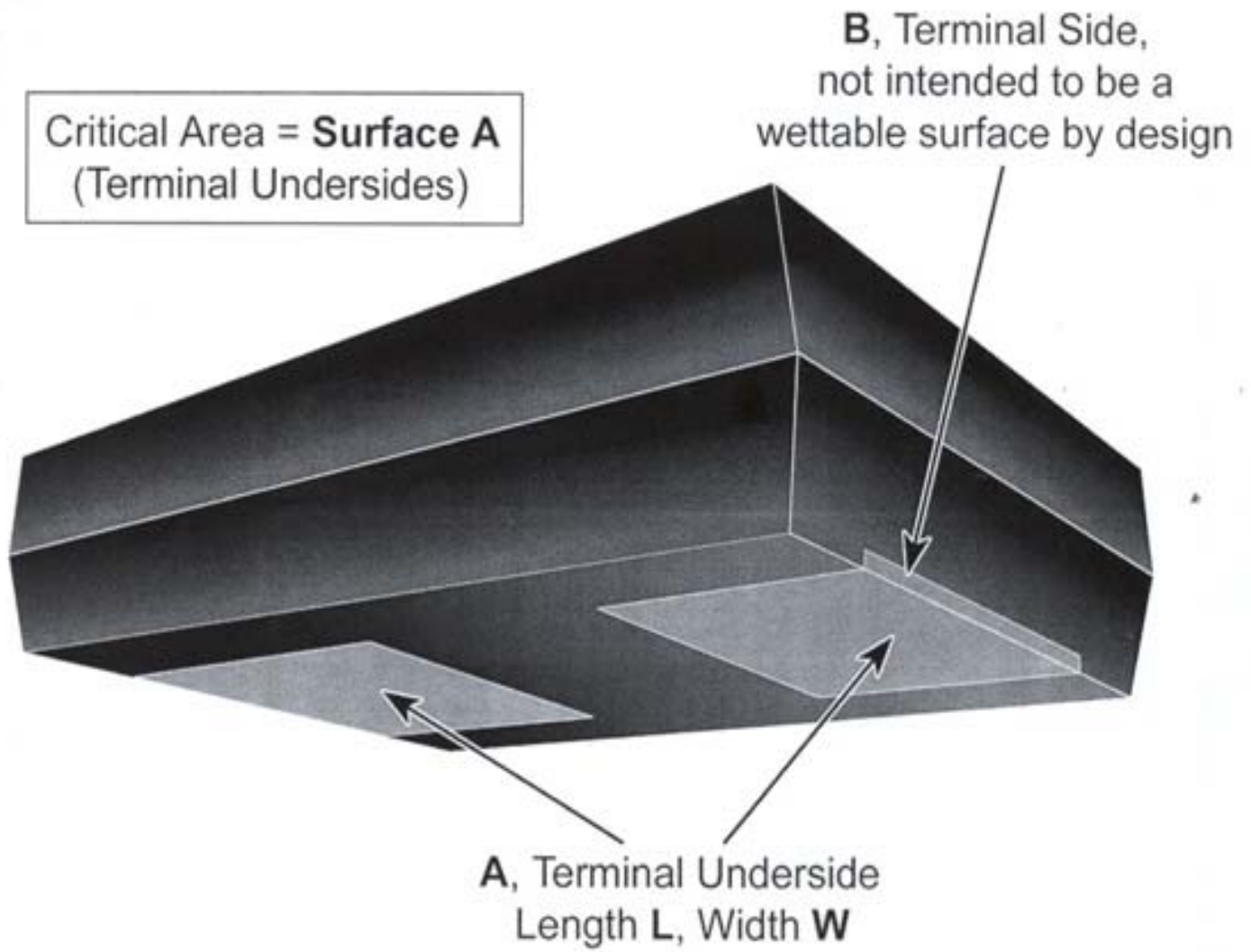


Figure A-9 Bottom-Only Termination Component

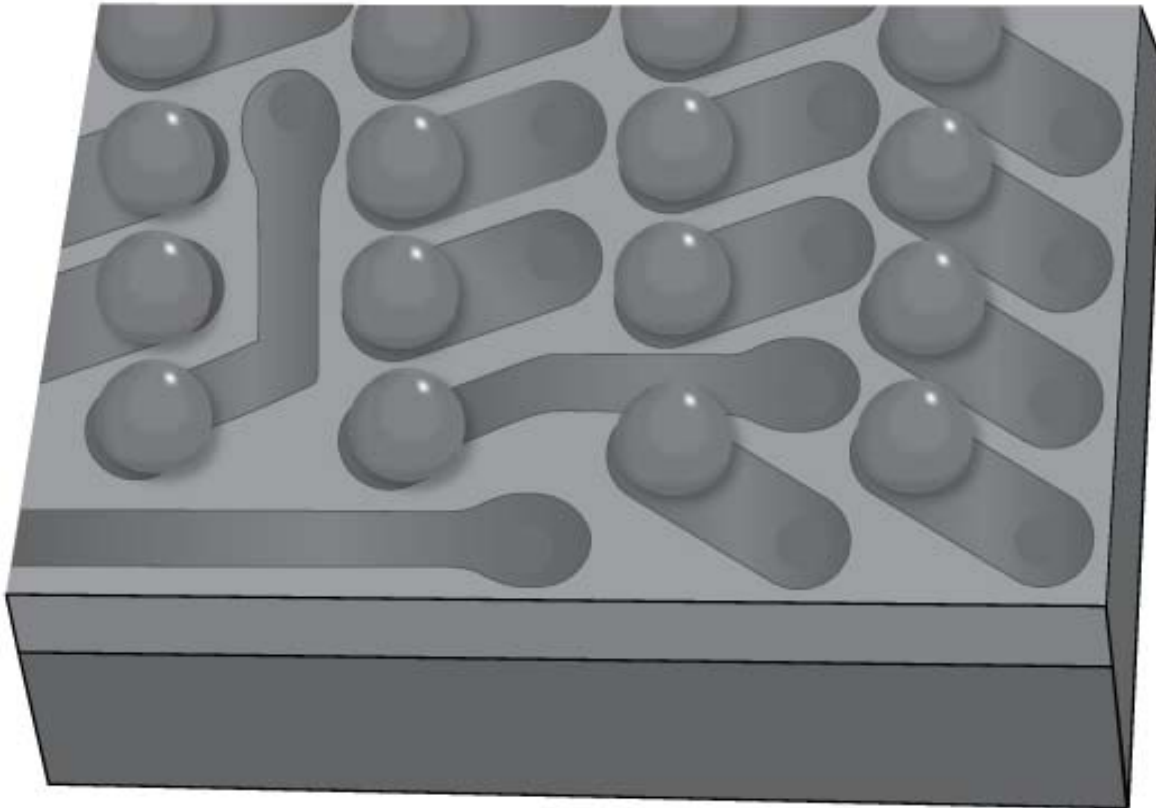


Figure A-10 Area Array Component Critical Surface

[Critical Surface: Each soldersphere **shall** have incorporated the solderpaste deposit (uniform, smooth solder with no dewetted areas)]. With Tests S and S1 – Surface Mount Process Simulation, the component leads **shall** be wetted in a consistent and unified manner, with no evidence of surface oxidation anomalies.

**Appendix B
Evaluation Aids**

B.1 Evaluation Aids for Tests A, C, A1 and C1

B.2 Round Leads The measurement of defects or the estimation of defect area percentage of the lead total surface area, is more difficult with round leads than it is with flat surface rectangular leads. For example, in viewing a cylindrical surface such as a round lead, a round diameter-sized defect when flat appears oval shaped and narrower in width than the visible surface of the lead in the transverse direction. ~~which is half of its circumference.~~

To aid the solderability test inspector in estimating the lead surface percentage after solderability testing, a guide sheet for different diameter leads is shown in Figure B-3. When 25.4 mm [1.0 in] of the lead surface of a 0.5 mm [0.02 in] diameter lead is inspected for solder coverage, 10 diameter size defects equal 5% of the total lead surface area. Numbers of half diameter size and quarter diameter size defects are also listed. Combinations of these sizes can also be totaled easily (see Figure B-1).

In considering areas not covered by a continuous, new solder coating and referring to the defined defects illustrated in Figure B-2, the visible areas of dewetting and nonwetting are applicable directly.

An example of what constitutes 5% of the dipped area is: six defects of 0.813 mm [0.032 in] diameter in a 25.4 mm [1.0 in] length of a 0.813 mm [0.032 in] diameter (No. 20 AWG) wires (see Figures B-3, B-4 and B-5).

B.3 Square Terminations Square terminations **shall** meet the requirements of the solderability coverage guide shown in Figure B-5.

B.4 Castellated Terminations Castellated terminations **shall** meet the same criterion as round leads.

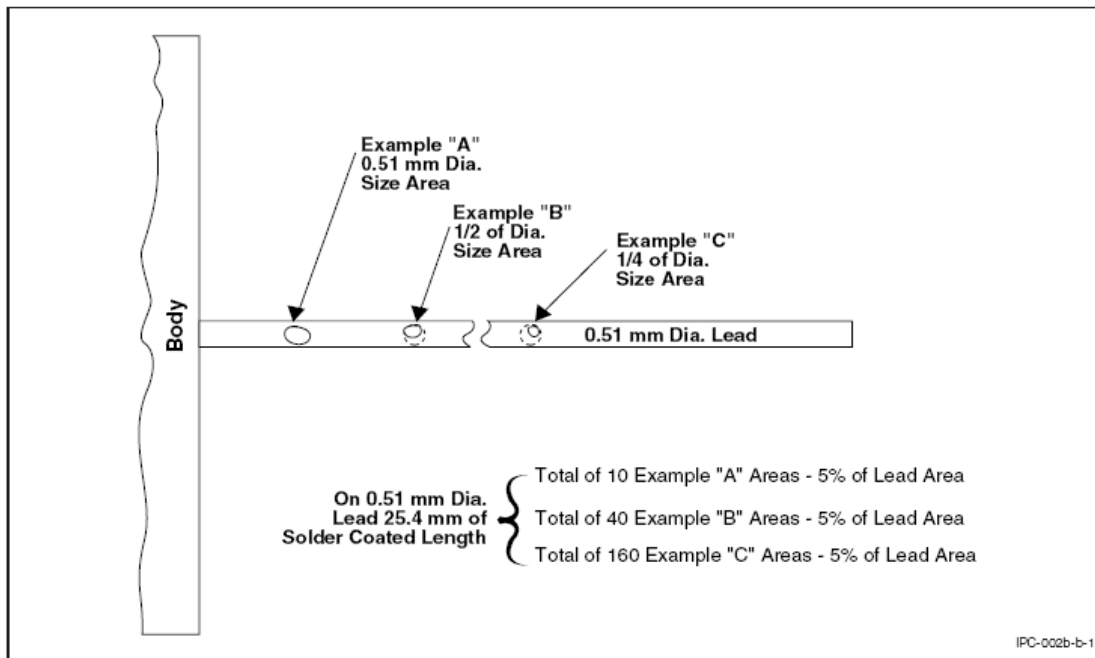


Figure B-1 Defect Size Aid

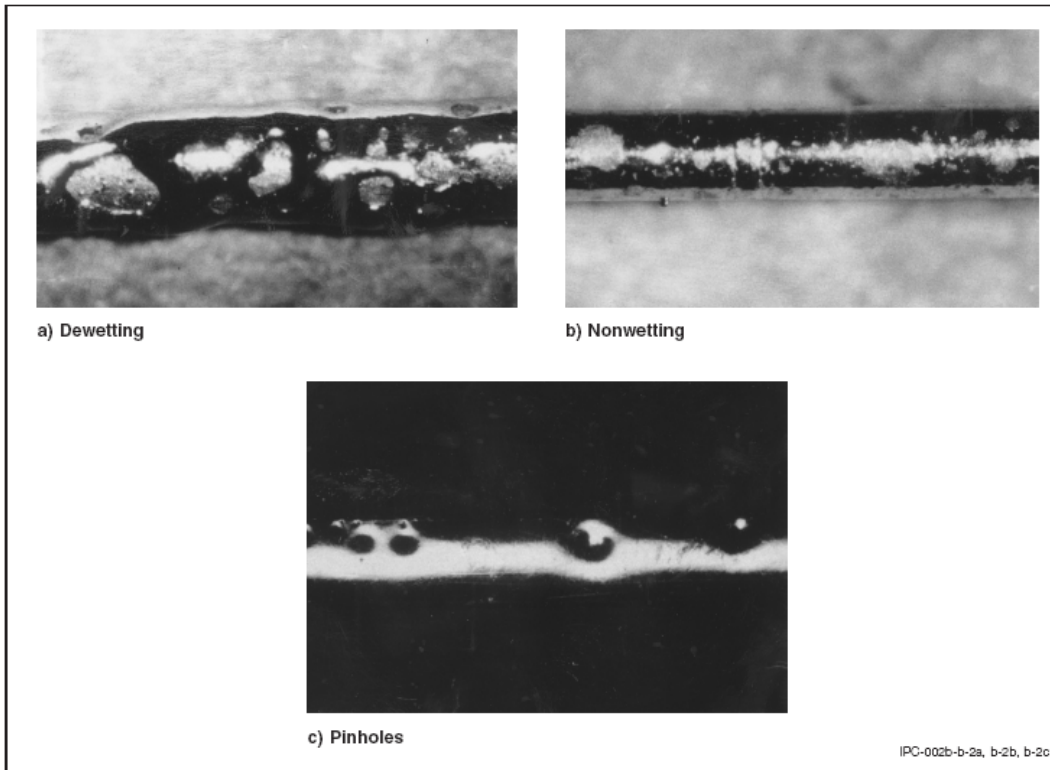


Figure B-2 Types of Solderability Defects

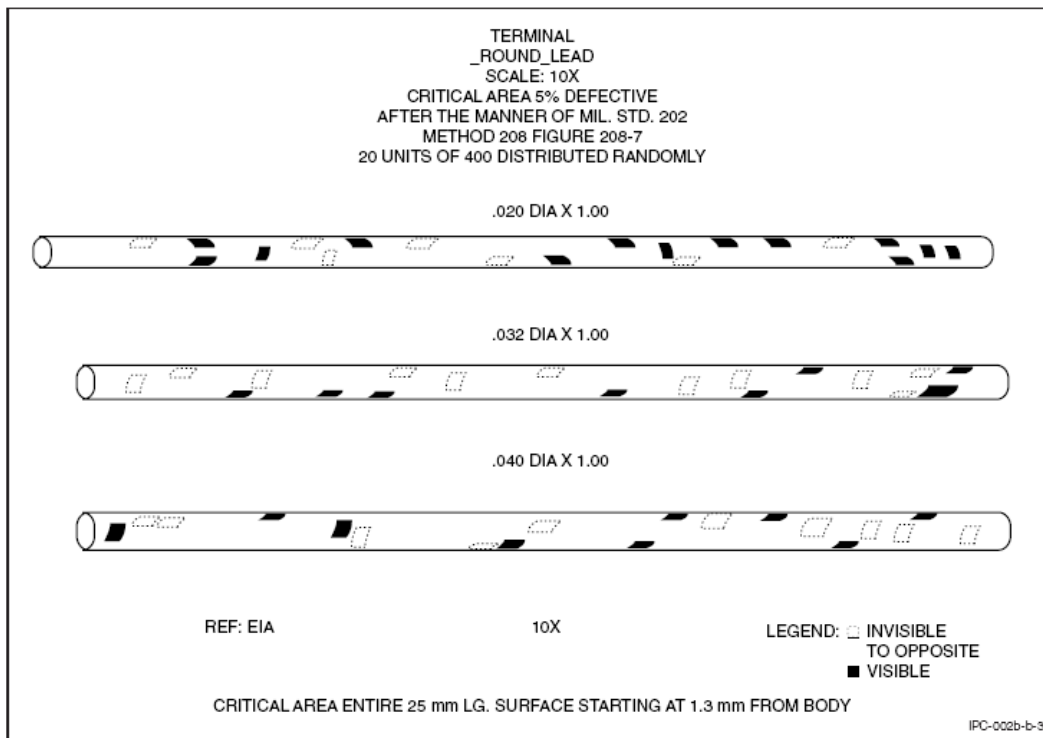


Figure B-3 Aids in Evaluation of 5% Allowable Area of Pin Holes

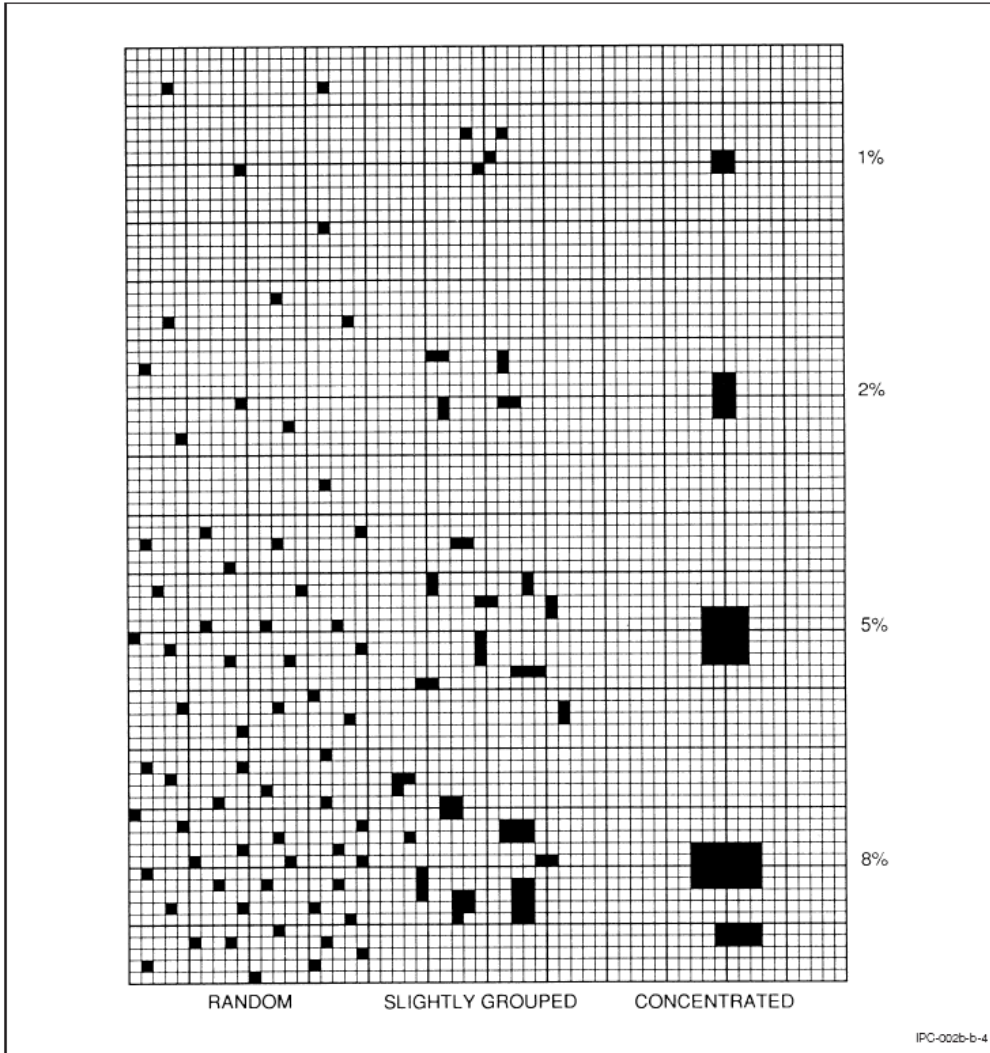


Figure B-4 Aid in Evaluation of 5% Allowable Area of Pin Holes

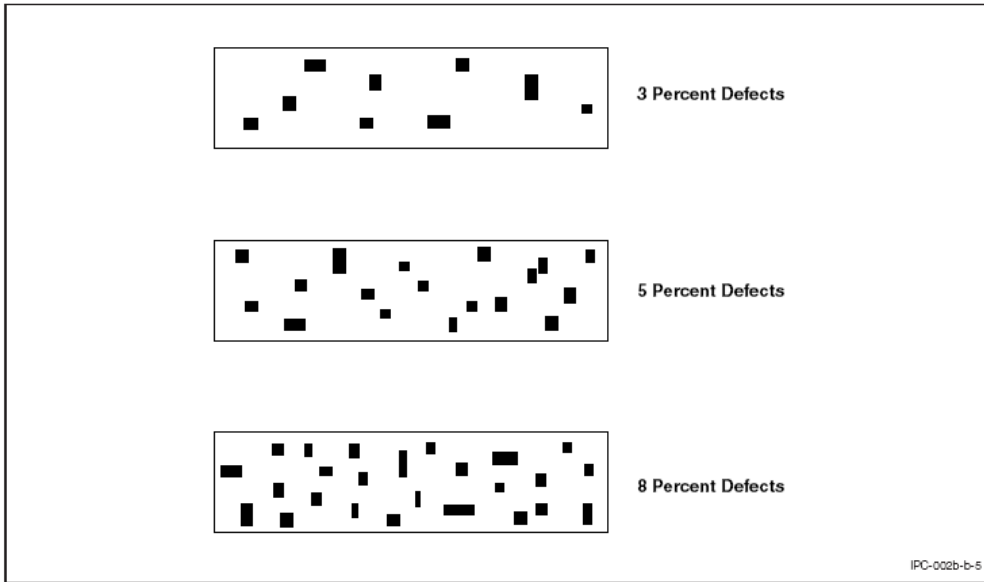


Figure B-5 Solderability Coverage Guide

Appendix C
Calculation of Maximum Theoretical Force

Maximum theoretical force is calculated using the procedure of Klein Wassink.¹

$$\text{Force (Max. Theoretical)} = (\gamma) (P) (\cosine \beta) - (d)(g)(V) = [0.4P - 0.08V] \text{ mN}$$

where:

P = the perimeter of the test specimen in millimeters, i.e., the length in millimeters of the solder/printed board or coupon pad (or hole)/air interface as measured at maximum depth of immersion.

V = The volume in cubic millimeters of the test specimen that resides below the solder/board air interface as measured at the maximum depth of immersion.

$$\gamma = \text{Surface tension of solder} = 0.4 \text{ mN/mm}$$

$$\gamma = \text{Surface tension of Pbfree solder} = 0.5 \text{ mN/mm}$$

$$\alpha = \text{Immersion angle of the board to the horizontal surface, i.e., } \alpha = 45^\circ$$

$$\beta = \text{Wetting angle of solder to the board under optimal conditions, i.e., } \beta = 0 \text{ and therefore the cosine } \beta = 1$$

$$d = \text{Density of solder at } 235^\circ\text{C,} = 8120 \text{ kg/m}^3 \text{ for Sn60/Pb40 Alloy}$$

$$d = \text{Density of solder at } 255^\circ\text{C} = 7410 \text{ kg/m}^3 \text{ for SAC305 Alloy}$$

$$g = \text{Gravitational constant} = 9.8 \times 10^3 \text{ mm/s}^2$$

Periphery and volumes are to be calculated using the nominal values provided by the device supplier in the package drawing and the angles and depths of immersion as described in the specification above. The TOTAL periphery and volume, i.e., the sum of all leads being immersed, is to be used in this calculation.

Figure C-1 below depicts a sample calculation for 132 I/O QFP.

Where:

$$w = \text{Lead width (nominal)} = 0.254 \text{ mm}$$

$$t = \text{Lead height (nominal)} = 0.1524 \text{ mm}$$

$$d = \text{Immersion depth} = 0.3 \text{ mm}$$

$$l = \text{Lead length immersed on bottom side at } 20^\circ \text{ angle and } 0.3 \text{ mm depth} = 0.877 \text{ mm}$$

$$m = \text{Lead length immersed on top side at } 20^\circ \text{ angle and } 0.3 \text{ mm depth} = 0.458 \text{ mm}$$

$$k = \text{Length of solder/lead/air interface along lead side} = 0.446 \text{ mm}$$

$$2k + 2w = \text{Total length per lead of solder periphery} = 0.892 + 0.508 = 1.4 \text{ mm}$$

$$P = \text{Total length of periphery per side (33 leads)} = 46.2 \text{ mm}$$

Hence:

$$\text{Total volume immersed per lead} = 0.254 \times 0.1524 \times 0.458 + 0.5(0.1524 \times 0.254 \times 0.419) = 0.0177 + 0.0081 = 0.0258 \text{ mm}^3$$

Therefore for an 132 I/O QFP, the maximum theoretical wetting force is:

For 33 leads (one side of an 132 I/O QFP) = $33 \times 0.0258 \text{ mm}^3 = 0.85$

Maximum Force = $(0.4 \times 46.2) - (0.08 \times 0.85) = 18.41 \text{ mN}$

And, for a part of 46.2 mm total periphery:

Maximum Force per length of interface = $399 \text{ } \mu\text{N}/\text{mm}$

The force measured on a part in the Set A criteria must therefore be greater than 9.2 mN or 200 $\mu\text{N}/\text{mm}$ at F2.

Note: All forces are referenced to the corrected zero axis and not the zero force line except for the Appendix D calculation (parameter AA).

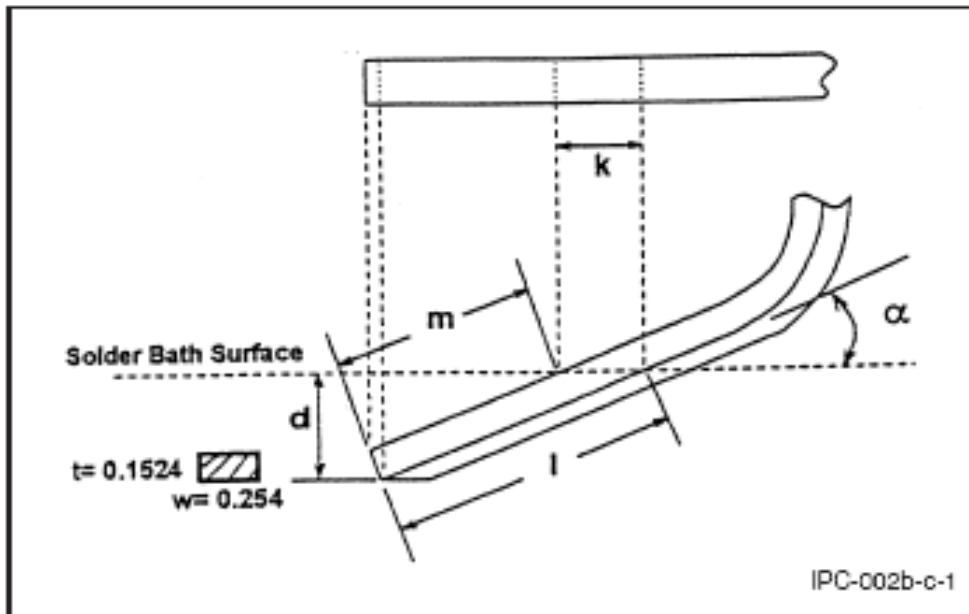


Figure C-1 Lead Periphery and Volume for a 132 I/O PQFP

1. R. J. Klein Wassink, "Soldering in Electronics," 2nd Edition, Electrochemical Publications, Ayr, Scotland, 1989, pp 308-309

Appendix D
Calculation of Integrated Value of Area of the Wetting Curve

The area is calculated using the maximum theoretical force (see Figure 4.18 or 4.19). Therefore, the area is given as:

Area = Wetting force x time – Buoyancy x time

Area = (3.0 sec. x Max. Theoretical Force) – 2.0 sec (ρ) (g) V

Area = (3.0 sec. x Max. Theoretical Force) – 2.0 sec x - (8.12 x 10⁻⁶ kg/mm³ x 9.8 x 10³ mm/s² x V)

The value V is the volume of the test specimen immersed in the solder bath as calculated in Appendix C. The maximum theoretical force is calculated as per Appendix C. The following assumptions are made:

1. The maximum buoyancy force holds for the whole two (2) seconds contributing a negative area of: the buoyancy force times two (2) seconds.
2. The test specimen essentially attains the full maximum theoretical force as it crosses the zero line at two (2) seconds and holds that value for the duration of the test, i.e., three (3) seconds.

V = Total Volume = 0.4 mm³

Maximum Theoretical Force: 3.97 mN

Area = (3.0 sec. x 3.97mN) - (2.0 sec. x 0.08 (kg/mm³ x mm/s²) x 0.4 mm³) = 11.91 mN x seconds - 0.064 (kg-mm/sec)

Since F= ma, then mN = kg x mm/sec² or kg = mNsec²/mm

Area = 11.91 mN x seconds - 0.064 (mNsec²/mm) x (mm/sec)

Area = 11.91 mN x seconds - 0.064 mN x seconds

Area = 11.85 mN x seconds

Appendix E Informative Annex

E.1 Test Equipment Sources The equipment sources described below represent those currently known to the industry. Users of this document are urged to submit additional source names as they become available, so that this list can be kept as current as possible.

E.1.1 Tests A, B, C, D, A1, B1, C1, D1

GEN3 Systems Limited (Formerly Concoat Systems) Unit B2, Armstrong Mall, Southwood Business Park, Farnborough, Hampshire GU14 0NR England. 011 44 12 5252 1500 www.gen3systems.com

HMP Soldermatics, P.O. Box 948, Canon City, CO 81215, (719) 275-1531

Malcomtech 26200 Industrial Blvd, Hayward CA 64545, (510)293-0580, www.malcomtech.com

Reef Engineering, Unit 6, Bancrofts Road, South Woodham Ferrers, Essex CM3 5UQ 01245 328123

Robotic Process Systems Inc., 23301 E. Mission Ave., Liberty Lake, WA 99019, (509)891-1680

E.1.2 Tests E, F, E1 & F1

Convey AB, Harpsundsvagen 113, S-12458 Bandhagen, Sweden 46 (0) 8 99 66 25

GEN3 Systems Limited (Formerly Concoat Systems) Unit B2, Armstrong Mall, Southwood Business Park, Farnborough, Hampshire GU14 0NR England. 011 44 12 5252 1500 www.gen3systems.com

Malcomtech 26200 Industrial Blvd, Hayward CA 64545, (510) 293-0580, www.malcomtech.com

Metronelec, 54, Route de Sartrouville - Le Montreal 78232 Le PECO Cedex, France (USA Distributor: Solderability Testing and Solutions Inc., 18 Wildrose Dr., Edgewood, KY 41017, (859) 331-0598, www.wettingbalance.com

Robotic Process Systems Inc., 23301 E. Mission Ave., Liberty Lake, WA 99019, (509)891-1680

E.1.3 Tests G & G1

GEN3 Systems Limited (Formerly Concoat Systems) Unit B2, Armstrong Mall, Southwood Business Park, Farnborough, Hampshire GU14 0NR England. 011 44 12 5252 1500 www.gen3systems.com

Metronelec, 54, Route de Sartrouville - Le Montreal 78232 Le PECO Cedex, France (USA Distributor: Solderability Testing and Solutions Inc., 18 Wildrose Dr., Edgewood, KY 41017, (859) 331-0598, www.wettingbalance.com

E.1.4 Steam Conditioning Equipment

H&H Engineering, Inc., 3612 Wood Duck Circle, Stockton, CA 95206

Metronelec, 54, Route de Sartrouville - Le Montreal 78232 Le PECO Cedex, France (USA Distributor: Solderability Testing and Solutions Inc., 18 Wildrose Dr., Edgewood, KY 41017, (859) 331-0598 www.wettingbalance.com

Mountaingate Engineering Inc., Campbell, CA 95008, (408) 866-5100

Robotic Process Systems Inc., 23301 E. Mission Ave., Liberty Lake, WA 99019, (509) 891-1680

Zentek Scientific Systems, 3520 Yale Way, Fremont, CA 94538, (510) 651-1581

E.1.5 Grid Reticles

Bender Associates, 5030 South Mill Avenue, Suite C-2, Tempe, AZ 85252, (602) 820-0900

E.2 Test Flux Product Sources The Test Flux product sources described below represent those currently known to the industry. Users of this document are urged to submit additional product source names as they become available, so that this list can be kept as current as possible.

AIM Solder {www.aimsolder.com} – Standard Flux #1 Product ID: RMA 202-25

Alpha Metals {www.alphametals.com} – Standard Flux #2

GEN3 Systems Limited {www.gen3systems.com} – Product ID's: SMNA – Standard Flux #1: Actiec 2 / – Standard Flux #2: Actiec 5

Kester {www.kester.com} – Standard Flux #1 Product ID: 182

Qualitek International, Inc. {www.qualitek.com} – Standard Flux #1 Product ID: 285-25

Solderability Testing and Solutions Inc. {www.wettingbalance.com} – Standard test flux 0.2% and Standard test flux 0.5%

Appendix F
J-STD-002/J-STD-003 Activated Solderability Test Flux Rationale Committee Letter

The current J-STD-002/J-STD-003 specification includes a departure in the test flux methodology used in past solderability testing. The table in paragraph 3.2.2 Flux is:

Table 3-1 Flux Compositions

| Constituent | Composition by Weight Percent | |
|--|-------------------------------|-------------|
| | Flux #1 | Flux #2 |
| Colophony | 25 ± 0.5 | 25 ± 0.5 |
| Diethylammonium Hydrochloride (CAS 660-68-4) | 0.15 ± 0.01 | 0.39 ± 0.01 |
| Isopropyl Alcohol (IPA) (CAS 67-63-0) | Balance | Balance |
| Weight of Chlorine as % of Solids | 0.2 | 0.5 |

The J-STD-002/J-STD-003 committees understood that any proposed change to the use of ROL0 (formerly designated type R) would be heavily scrutinized and would require test data showing the applicability of using a standard activated flux composition. The J-STD-002/J-STD-003 committees have spent significant resources working this flux change issue, discussing the chemistry details and conducting multi-company Design of Experiment investigations. The J-STD-002 committee chairmen, Dave Hillman [Rockwell Collins], Doug Romm [Texas Instruments], Mark Kwoka [Intersil] and Jack McCullen [Intel], feel that the committee has compiled a significant data set and have held thorough topic discussions supporting the proposed flux material change. The four rationales for proposing/supporting the flux change are summarized below:

1) A Proactive Solderability Testing Approach To The Implementation of Non-Tin Finishes

A number of industry studies (1996 NEMI Surface Finishes Task Group Report, 1997 NCMS Lead-Free Solder Project, 2000 National Physical Laboratory CMMT (A) 284 Report) have shown that an incompatibility of “R type” flux with non-tin surface finishes such as palladium, organic solderability preservatives (OSPs), and immersion gold. The introduction of these various metallic surface finishes on components and printed wiring boards is no longer the exception but has/is quickly becoming the norm. The use of a “R type” flux containing only naturally occurring activators has resulted in producing “false negative” solderability test results which impact both the component/board fabricator and the assembler negatively in terms of cost and schedule.

2) Reduced Solderability Test Variability

The J-STD-002/003 solderability committees enlisted the assistance of Dr. Carol Handwerker and the resources of the National Institute of Standards & Technology (NIST) to investigate/compare a standard activated flux composition versus the “R type” flux composition. A detailed statistical analysis by Bill Russell, Raytheon Systems, and NIST statisticians revealed the use of a standard activated flux composition greatly reduced the amount of solderability test variation. One of the major goals of the J-STD-002/003 solderability committees is to develop test methods and standards which promote consistency across the industry.

3) Concerns of A Loss of Solderability Assessment Safety Margin

The two major historical rationale for using an “R type” flux: 1) colophony or rosin contains only naturally occurring flux activator constituents and thus is not subjected to the problems/complications of chemistry formulas by the flux supplier; 2) it was an accepted industry acknowledged fact that, if a component or printed wiring board surface was found to have acceptable solderability test results using “R type” flux, then the more active flux formulations used in the assembly process would produce acceptable solder process results. This solderability assessment safety margin was a self imposed, industry consensus decision. The J-STD-002/003 committees understood the historical relevance behind the decision to use “R type” flux and had an equally strong desire to maintain a solderability assessment safety cushion. However, committees fielded a number of industry inputs to reassess the solderability flux composition based on the technology improvements in surface finishes, improvements in the flux chemistry formulations from flux suppliers, and the desire to not have excessive safety margins which would impact cost and schedule in a non-value added manner. The committees conducted a number of tests (Wenger, Kwoka, ACI) demonstrating, using a specific standard level of activation on real world, industry supplied component and printed wiring board cases, that the occurrence of a “false acceptable” solderability test result was extremely low. There was no case that exhibited a “pass ROL1 test – fail ROL0 test – Fail during board assembly” sequence. In fact, the use of both ROL1 and ROL0 are more likely to create a “false reject “dip and look solderability test result when compared to board level soldering performance.

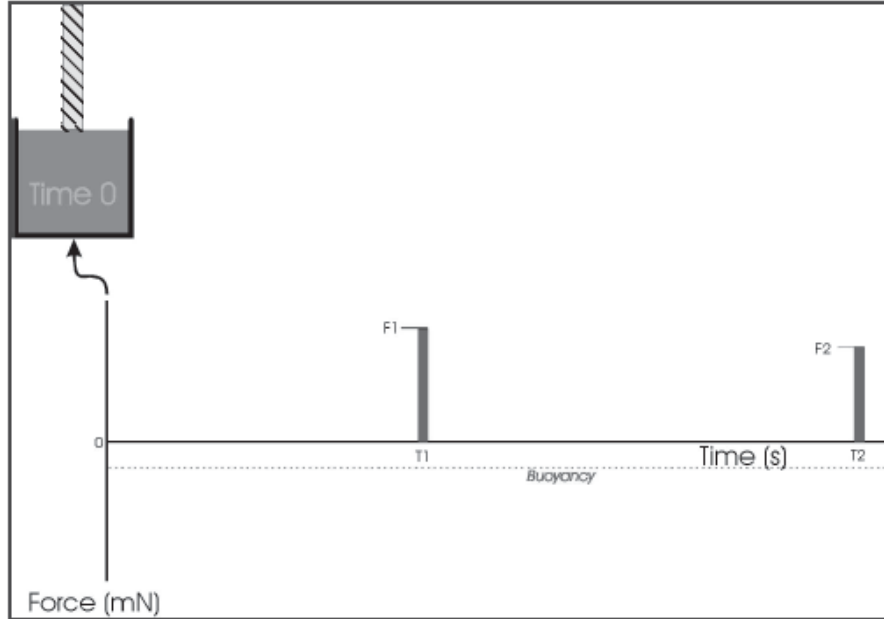
4) Standardization of Solderability Test Flux Composition On A Global Scale

A second major goal of the J-STD-002/003 solderability committees is to develop test methods and standards which promote global standardization for the electronics industry. The standard activated flux composition selected and tested by the committees has been utilized in the International Electrotechnical Commission (IEC) 60068-2-20 Soldering specification. The IEC specification is successfully utilized for solderability testing. Having compatibility of flux composition requirements between the J-STD-002/003 specification and the IEC specifications is a win-win situation for electronics assemblers and component/printed wiring board fabricators.

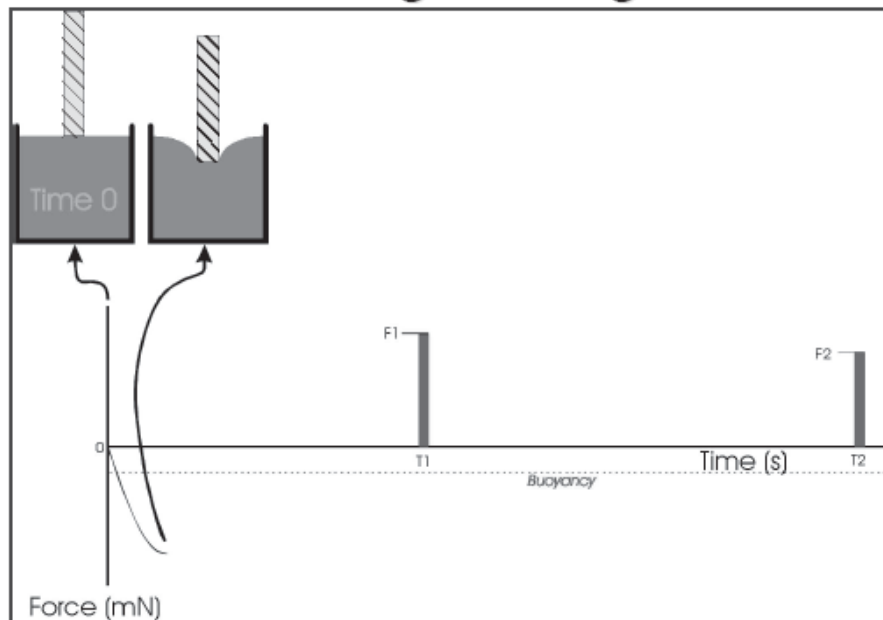
A number of the major flux chemistry suppliers have been queried on the electronics industry’s ability to purchase the standard activated flux composition, and a positive response was received. If you have any questions please contact the IPC Technical Staff to obtain additional answers/clarification.

Appendix G
Graphical Representations: Progression of Solder Wetting
Curve Parameters As Measured By Wetting Balance Testing

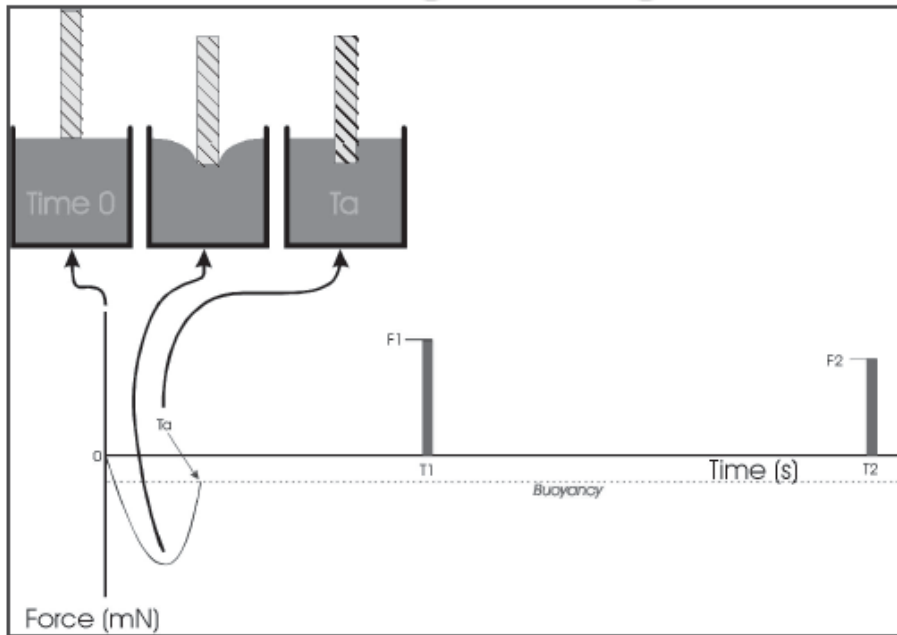
Understanding Wetting Curves



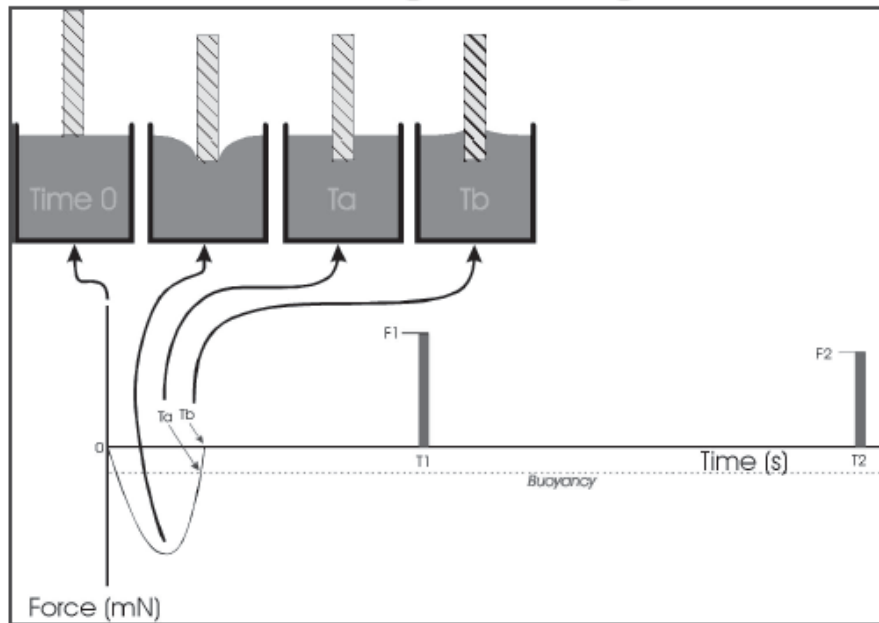
Understanding Wetting Curves



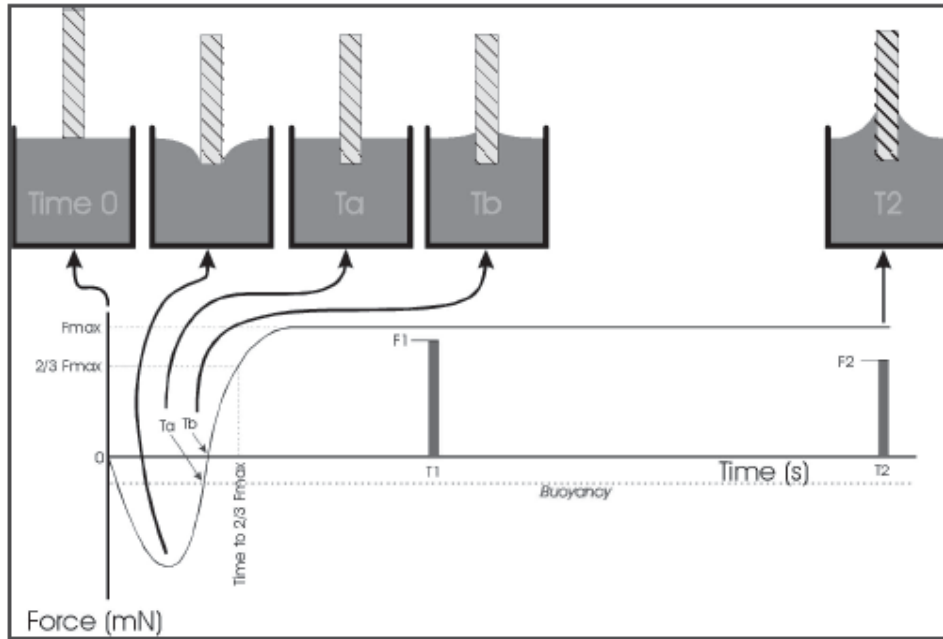
Understanding Wetting Curves



Understanding Wetting Curves



Understanding Wetting Curves



The net of the parameters measured from the progression of the five steps encountered during the wetting balance testing, as highlighted above:

Typical Wetting Curve

