



Electronic Components, Assemblies & Materials Association

AGENDA
P-1 Committee on Resistive Devices
October 5, 2004
4PM – 6PM
San Diego, CA

- 1.0 Introductions
- 2.0 Committee Organizations and Procedures
 - 2.1 Membership
 - 2.2 Approval of Agenda
 - 2.3 Approval of April 2004 Meeting Minutes
 - 2.4 Correspondence
 - 2.5 Review of Committee's Scope – “ All types of resistive components regardless of technology. Includes composition, film, wirewound, thermistors, attenuators, networks, and chip resistors and exclude Integrated Passive Devices.”
 - 2.6 DSCC Report
- 3.0 Old Business
 - 3.1 Follow up of the proposed changes for ESD tests in EIA/IS-703 – Brian Piscitelli and Ed Mikoski
 - 3.1.1 Requested PN number and sent electronic version to Ed for updating of the 0.25% to “Per Users Spec.”
 - 3.2 Review Comments from the ballots of both EIA-576-A, EIA-575-A

- 3.2.1 Need Panasonic's Vote on Both
- 3.3 Review of Resistor outlines to the newly formatted/web-based PDP-100
- 3.4 Review proposed changes to EIA-886 and EIA-887
 - 3.4.1 – Requested PN Numbers
- 3.5 Start the process of generating a thin film on ceramic resistor array spec.
 - 3.5.1 – Requested PN Number
- 3.6 Review open P-1 projects
- 3.7 Pb-free Updates
 - 3.7.1 – Update from Intel on JEITA ETR-7021 and their Pb-free plans
- 3.8 Other
- 4.0 New Business
 - 4.1 Other
- 5.0 Next Meeting
- 6.0 Adjournment