

AGENDA

Electronic Components, Assemblies & Materials Association

Engineering Sub-Committee on Ceramic Capacitors (P-2.1)

Wednesday, October 19, 2005

Memphis, TN

P-2.1 Ceramic Capacitor Sub-committee

1.0 Introductions

2.0 Committee Organization and Procedures

- 2.1 Membership
- 2.2 Approval of Agenda
- 2.3 Approval of May 2005 Meeting Minutes
- 2.4 Correspondence
- 2.5 Report of task groups, committees, and DSCC efforts

3.0 Old Business

- 3.1 Review status of EIA-595-A (PN 5094)- Lee, Kemet
- 3.2 Review and resolve comments for EIA-469-D (SP 4621)- Pollock, Presidio
- 3.3 Status of EIA-198-F "Ceramic Dielectric Capacitors Classes I, II, III, & IV"
 - 3.3.1 Part III/3 standard SMD (PN 5099)- Cannon, TDK
 - 3.3.2 Part III/9 HV radial leaded (PN?)- Lee, Kemet and Pollock, Presidio
 - 3.3.3 Part III/9 HV SMD (PN?)- Malhotra, Murata
 - 3.3.4 Part III/10 Reverse geometry
 - 3.3.5 Part III/11 Multiple terminal geometries
- 3.4 Status of IEC/EIA 521 (PN 5095)- Cannon, TDK
- 3.5 Rated vs. actual capacitance @ operating conditions- Wojewoda, Intel
- 3.6 Review of CB19, tin whisker position statement.
- 3.7 Review any additions to PDP-100
- 3.8 Five-year document review.
- 3.9 Review Active Projects file

4.0 New Business

- 4.1 Elect new chairman
- 4.2 Other new business

5.0 Next meeting

- 5.1 Florida

6.0 Adjournment

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